

# Low Cost Power Converter with Improved Performance for Switched Reluctance Motor Drives

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**ABSTRACT**— The Switched Reluctance Motor has inherent mechanical strength without rotor winding and permanent magnet. To improve the power factor correction and to perform the commutation faster, several topologies are analyzed such as buck, boost and buck boost, dc-dc converter. This paper presents the boost converter with PFC to improve the performance of the converter with high rectified output. The proposed converter performs the phase current commutation faster. This paper also presents new controller design for Switched Reluctance Motor. The PI controller is used to track the required line current. The simulation of the SRM along with the Asymmetric Bridge converter and proposed converters are performed by the MATLAB software. The results are analyzed and compared, which shows that the new converter performs the phase current commutation faster than the Asymmetric Bridge converter.

**KEYWORDS**— Switched Reluctance Motor, Boost converter, Power Factor Correction, Commutation, Asymmetric Bridge Rectifier.

## I. INTRODUCTION

The Switched Reluctance Motor is doubly salient and singly excited motor. The SRM becomes more popular because it has some advantages over other motors. The advantages of the SRM are low cost, robustness, reduced size, high fault tolerance, efficiency over wide speed range, production and less volume compared to the other motor drives<sup>[1]</sup>. The winding is

present only on the stator, not on the rotor. The mechanical simplicity of the SRM has some drawbacks. The major drawbacks of the SRM are acoustic noise and the torque ripples. The torque ripples in the SRM are arises, due to the phase current commutation. If the converter has the ability to commutate the phase current faster, then the torque ripples can be reduced. So the converter plays an important role in the Switched Reluctance motor. The converters used for the SRM drives are C dump converter, R dump converter, bifilar, split dc supply and asymmetric bridge converter. The C dump converter is used, because the capacitor in this converter is used to store the energy<sup>[2]</sup>. The SRMs also offers, some advantages. They can be reliable, since each phase of the SRM is independent of magnetically and electrically from the other motor phases. Very high speeds are also achieved, due to the lack of conductors or magnets on the rotor.

The Switched Reluctance Motors are simple machine construction, brushless, fault tolerant, free maintenance and rugged the Switched Reluctance Motors are used in various applications such as washers, dryers, industrial blowers, electrical vehicles, aerospace and centrifugal pumps.

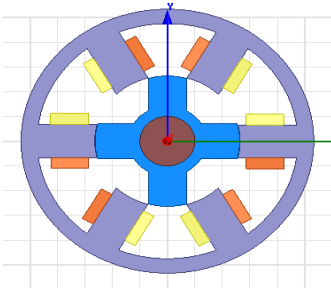


Fig.1 6/4 SRM

The SRM drives are used to reduce the torque ripple, at the higher speeds, but the commutation of the phase current is not so faster<sup>[3]</sup>. A new control technique is designed to control the SRM at high speeds. The converter used for the SRM drives is for switching the phase currents, this leads to the distorted current waveform. This would results in non unity power factor, for achieving the unity power factor the power factor correction technique is used in Switched Reluctance Motor<sup>[4]</sup>.

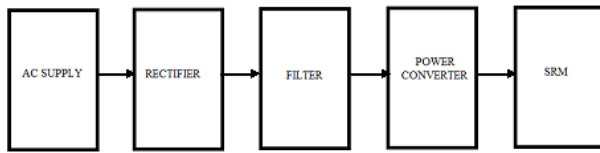


Fig.2 Block diagram of SRM

The torque and the electrical equation of the Switched Reluctance motor are

$$T = \frac{1}{2} i^2 \frac{\partial L}{\partial \theta} \quad (1)$$

$$V = R_s i + L(\theta, i) \frac{di}{dt} + e \quad (2)$$

The induced emf  $e$ , is expressed as

$$e = \frac{dL(\theta, i)}{d\theta} \omega_m i \quad (3)$$

The step angle or the stroke angle is ,

$$\epsilon = \frac{2\pi}{Ns \cdot Nr} \times 2 \quad (4)$$

$N_s$ =Number of stator poles  
 $N_r$ =Number of rotor poles

## II. CONVERTER SELECTION

### A. Asymmetric Half Bridge Rectifier

The most widely used converter for Switched Reluctance Motor is the asymmetric bridge rectifier. In this, the winding is connected between the two switches. Fig.3 shows the asymmetric bridge rectifier, which is most widely used for Switched Reluctance Motor<sup>[5]</sup>. In this design, each phase winding uses two switches. The

switch S2 is in charge of commutation, while the switch S1 is used to perform the PWM switching control.

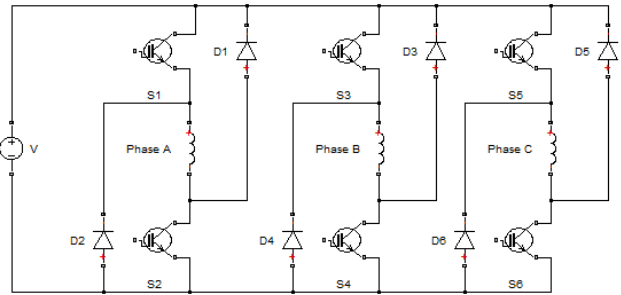


Fig.3 Asymmetric Bridge Rectifier

The switch S2 is in charge of commutation, while the switch S1 is used to perform the PWM switching control. In asymmetric half bridge design each phase can be controlled independently. From the current controlled PWM switching control, there are three modes of operation. They are, Excitation mode, Freewheeling mode and de-energization modes are shown in the Fig.4.

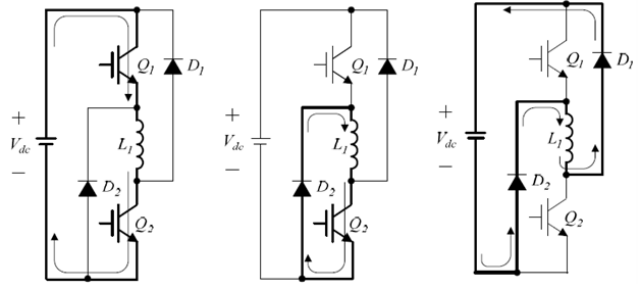


Fig.4 Modes of operation

### B. A New Proposed SRM Drive Converter

The new proposed Switched Reluctance Motor drive topology. The proposed SRM drive looks like a R dump converter and it has higher efficiency than the R dump converter. The proposed converter operates like a C dump converter and it performs phase current commutation faster than the C dump converter. The operation of the proposed converter is very simple and also the phase inductance energy is recovered.

The various operating modes of the proposed converter are,

- i) Magnetization mode
- ii) Demagnetization mode
- iii) Overlap of two phases : mode 1
- iv) Overlap of two phases : mode 2

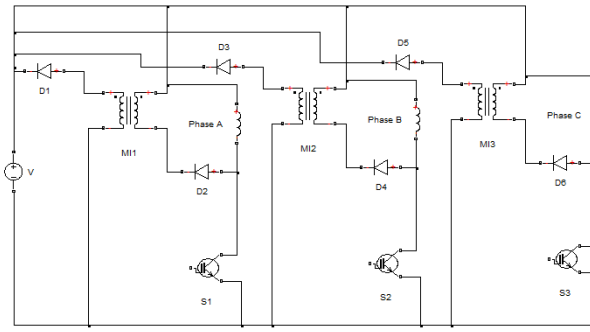


Fig.5 Proposed SRM drive Converter

The magnetization mode is shown in the Fig.6(a). In this mode, the switch T1 turns ON to magnetize phase 'a'. When the switch T1 turns ON, the energy from the source is transferred to the phase winding and the current in the phase inductance increases. And also in this mode, the magnetizing inductance of the coupled inductors is not reset yet, then the diode D1 conducts the magnetizing inductance current of the coupled inductors and the input voltage resets the inductor. The diode D1 turns on, to reset the magnetizing inductance of the coupled inductors. Then the reset of the coupled inductors and the magnetizing inductance is similar for the other phases.

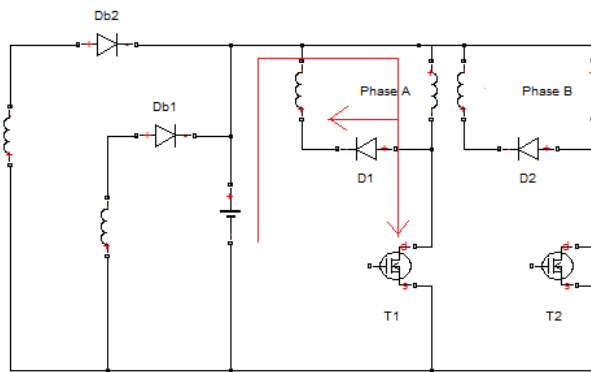
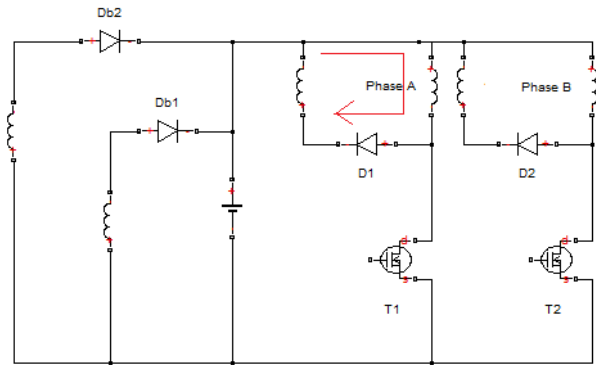


Fig.6(a) Proposed converter, Magnetization mode

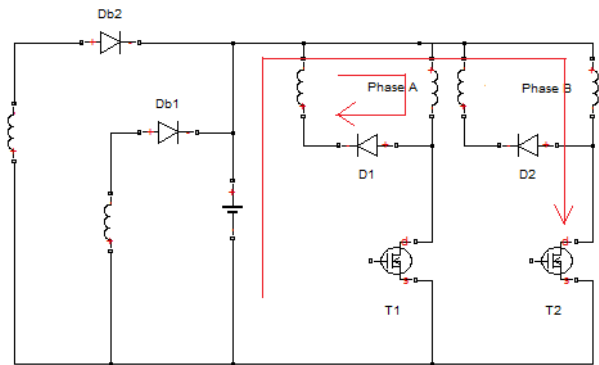
Fig. 6(b) shows the demagnetization mode. The demagnetization mode starts, when the phase current reaches the reference and T1 is turns off. In this mode the diode D1 turns on, Then the voltage across the phase winding is reversed. When the diodes D1 and Db1 turns on, the negative voltage is placed across the phase winding in proportion to coupling ratio which accelerates phase current commutation.



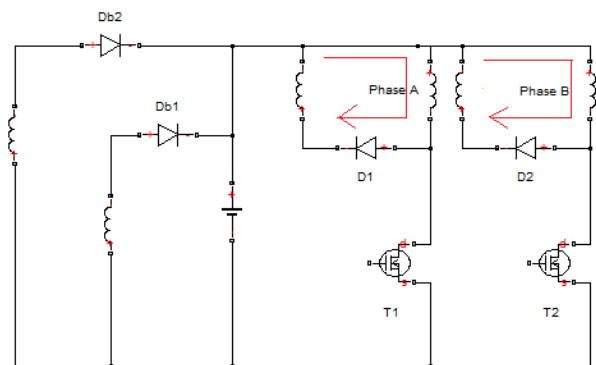
(b) Demagnetization mode

Fig.6(b) Proposed converter, Demagnetization mode

Then the two overlapping modes of the stator phase currents is shown in the Fig.6(c&d). In the first overlapping modes, the phase inductance 'a' is demagnetized and phase 'b' is magnetized. In the second overlapping mode, both 'a' and 'b' phases are demagnetized. From these observation, the converter has the ability for separately controlled phase currents.



(c) Overlap of two Phases : mode 1



(d) Overlap of two Phases : mode 2

Fig.6(c&d) Overlap of two phases - mode 1 and mode 2

### III. POWER FACTOR CORRECTION

The most widely used power factor correction technique is adding a resistor to a supply side. A sinusoidal reference is needed when converter takes the sinusoidal current from the source. There is some control strategies which force the input current to follow the current reference. The main objective of the power factor

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correction is to bring the unity power factor closer. The inductive loads are the main cause for the poor power factor, and the poor power factor is corrected by means of adding the capacitors to the circuit. Adding capacitor to the circuit is the simplest technique for correcting the poor power factor. The fixed power factor correction is the capacitor is connected parallel to the device.

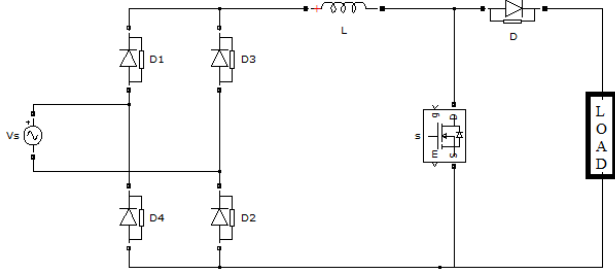


Fig.7 Power factor correction technique using boost converter

The boost type converters are proposed for improving the characteristics of the converter. The important step is to determine the suitable converter for a given application. For this several methods are considered, such as robustness, power density, efficiency, cost, and complexity. The PFC with the boost converter is the advanced power factor correction technique. It consists of the power factor correction, active power filter and AC-DC converter. A PI control is used for tracking the required line current command.

Fig.7 shows the diode rectifier associated with the boost converter which is widely used in active PFC. The principle behind this is the combination of the bridge rectifier and a AC-DC converter with filter and the energy storage elements can be extended to the other topologies, such as buck, buck boost and cuk converter. From the topologies said above, boost converter is very simple and it allows the low distorted input currents, with unity power factor using different dedicated control techniques such as the hysteresis, PI control techniques.

## IV. MATLAB/SIMULINK MODELING AND SIMULATION RESULTS

The simulation is carried out with different cases, the simulation results of the SRM drive with the proposed converter is compared with the results of the Switched Reluctance Motor which uses the asymmetric bridge rectifier and the power factor correction.

### A. The Asymmetric Bridge Converter Fed SRM

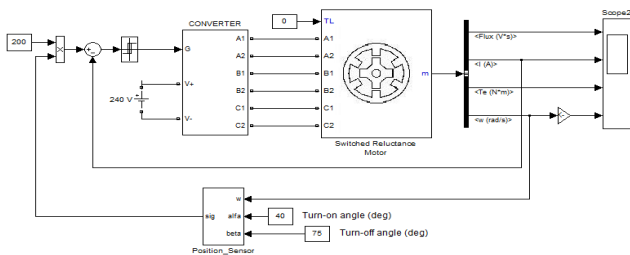


Fig.8 The Asymmetric Bridge Converter Fed SRM Drive

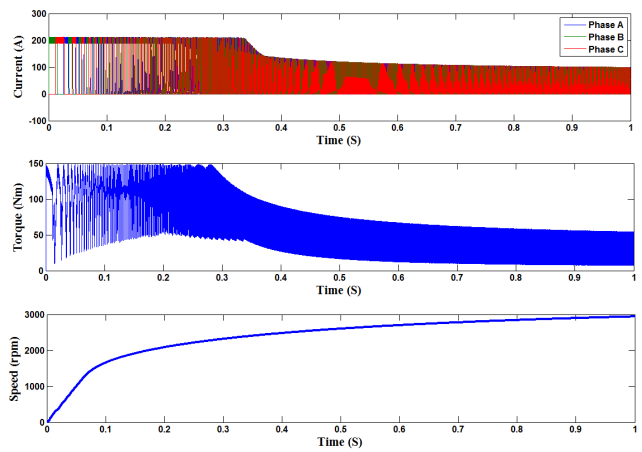


Fig.9 Phase Currents, Electromagnetic Torque, Speed waveforms of SRM

### B. The Proposed Converter with SRM

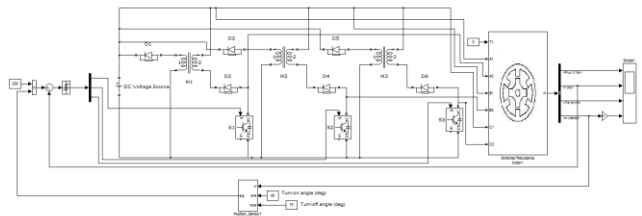


Fig.10 Proposed 3 Phase Converter Fed SRM Drive

### C. Power Factor Correction Technique

Fig. 12 and Fig. 13 shows the input output voltage waveform and current waveforms of the rectified topology without filter.

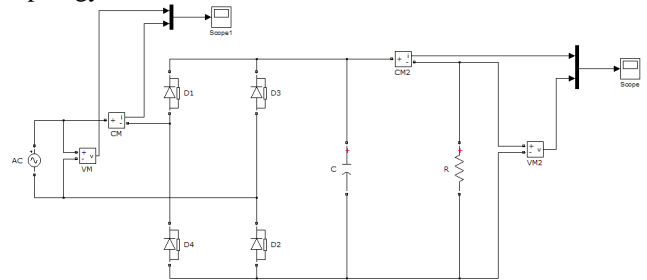


Fig.11 Rectifier Topology

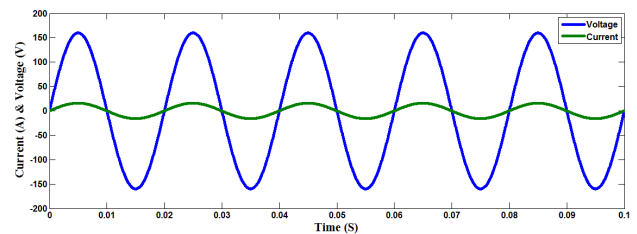
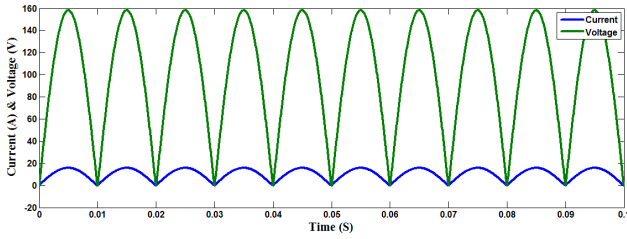
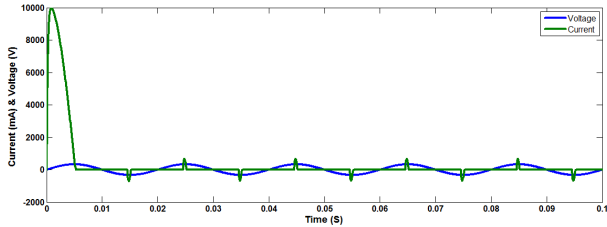


Fig.12 Input Current and Voltage waveforms of Rectified Topology without Filter

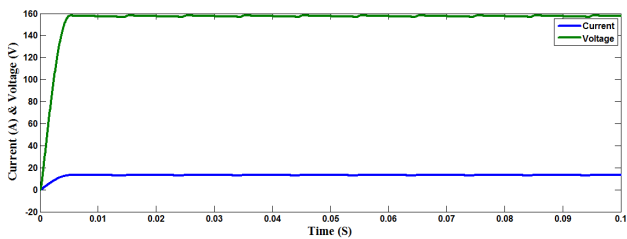
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**Fig.13** Output Current and Voltage waveforms of Rectified Topology without Filter

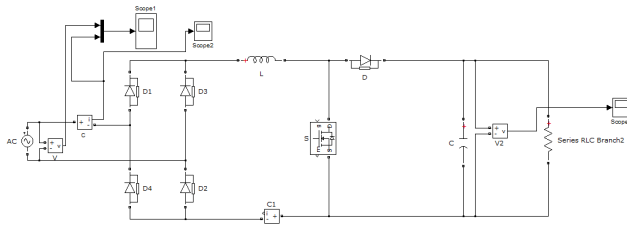


**Fig.14** Input Current and Voltage waveforms of Rectified Topology with Filter

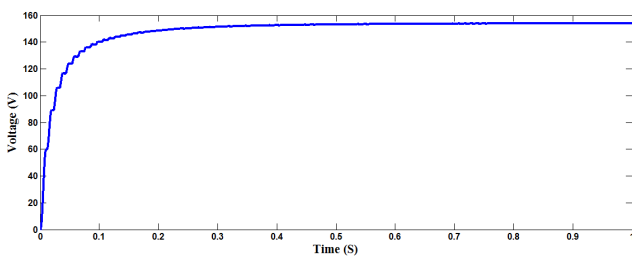


**Fig.15** Output Voltage waveforms of Rectified Topology with Filter

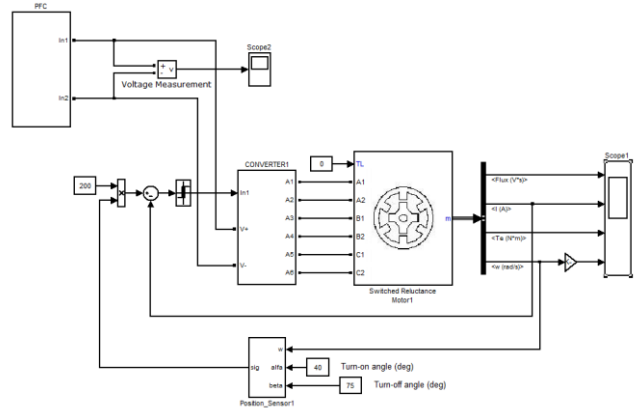
Fig.14 and Fig.15 shows the input and output current and voltage waveforms of the rectifier with filter. From the Fig.14, due to the filter the source current distorts and gets non unity power factor. This condition is improved by means of full bridge rectifier and the boost converter.



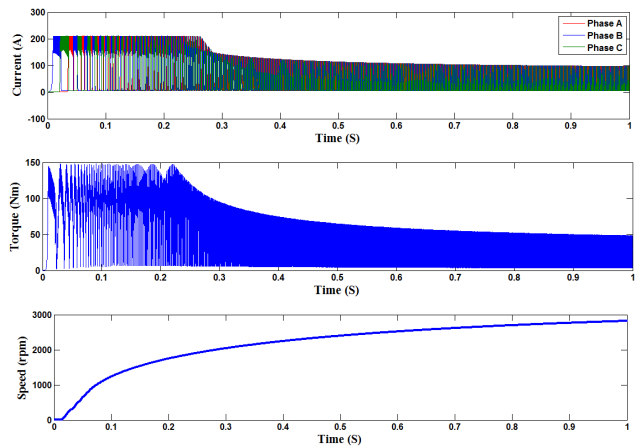
**Fig.16** Simulink Model of Power Factor Correction



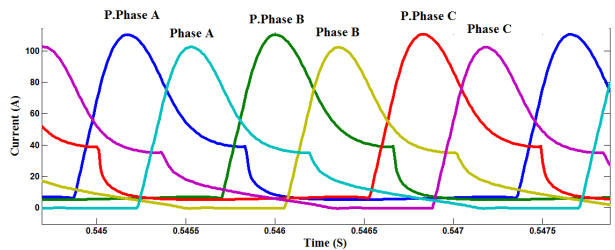
**Fig.17** Output Voltage of Proposed Converter Fed SRM Drive with Power Factor Correction



**Fig.18** Proposed SRM Drive with Power Factor Correction using MATLAB



**Fig.19** Phase Currents, Electromagnetic Torque, Speed waveforms of Proposed SRM Drive with Power Factor Correction



**Fig.20** Comparison of phase current from the Asymmetric bridge rectifier and the new proposed converter

Fig.20 shows phase currents of the Asymmetric bridge rectifier and the proposed converter. From this, the Proposed converter commutates faster than the Asymmetric bridge converter.

## V. CONCLUSION

The power factor correction is achieved by means of boost converter and also a new switched reluctance motor drive is introduced in this paper. The new proposed converter topology for the SRM is analyzed

and its various operating modes are discussed. For each motor phase one switch is used in this converter. To achieve a high efficiency, the phase inductance energy is recovered in the proposed converter. In this paper the asymmetric half bridge rectifier and the new proposed converter for the Switched Reluctance Motor drive is analyzed. From this analysis the proposed converter suits better for the Switched Reluctance Motor drive by reducing the number of switches and the power factor correction. The simulation results of both the asymmetric bridge rectifier and new proposed converter fed Switched Reluctance Motor drive is presented in this paper.

### Appendix I

#### Motor Parameters

Stator pole	6
Rotor pole	4
Phases	3
Voltage	(0-160)V DC
Power	1.2 KW
Current	8 A
Stator pole arc	29°
Rotor pole arc	32°
Stator outer diameter	162mm
Stator inner diameter	80mm
Shaft diameter	25mm
Stack length	90mm

### REFERENCES

- [1] R. Krishnan (2001), "Switched Reluctance Motor Drives: Modeling, Simulation, Analysis, Design, and Applications," Industrial Electronics Series, CRC Press.
- [2] Miller, T.J.E. et al. (Aug 4 1987), "Regenerative Unipolar Converter for Switched Reluctance Motors Using One Switching Device per Phase," U.S. Patent, No. 4, 684,867.
- [3] J. Liang, D.-H. Lee, J.-W. Ahn (2009), "Direct instantaneous torque control of switched reluctance machines using 4-level converters," IET Electric Power Appl, Vol.3, No.4, pp. 313-323.
- [4] G. Venkatesan, R. Arumugam, M. Vasudevan, S. Paramasivam and S. Vijayan (2006), "Modeling and Simulation of a Novel Switched Reluctance Motor Drive System with Power Factor Improvement," American Journal of Applied Sciences, Vol. 3, No. 1, 1649-1654.
- [5] Slobodan Vukosavic And Victor R. Stefanovic (1991), "SRM Inverter Topologies: A Comparative Evaluation," IEEE Transactions On Industry Applications, Nov/Dec. 1991, 27 (6), pp 946-958.
- [6] Ehsani, M., J.T. Bass, T.J.E. Miller, and R.L (1987) Steigerwald, Development of a unipolar converter for variable reluctance motor drives, IEEE Trans. Ind. Appl., Vol. 23, No. 3, pp. 545-55.