

# Analysis and Design of Common Mode Feedback Circuit for a Delta Sigma Modulator in 180 nm Technology

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ABSTRACT: The paper involves the simulation and design of a fully differential common mode feedback circuit for a two stage Oper`ational Transconductance Amplifier. This OTA serves as the loop filter for a delta sigma modulator in 180nm Technology. Simulations were performed in Cadence software. Step response and ac analysis of the OTA was also conducted. The nonlinearity characteristics of OTA were studied by performing Harmonic Distortion Analysis.

Keywords: common mode feedback, harmonic distortion, miller compensation, fully differential.

# I. INTRODUCTION

Fully differential circuits process signals fully-differentially without any control over the common mode potential. It is the common mode feedback (CMFB) circuit that keeps the common mode potential stable. Through the feedback action, the detected common-mode voltage is kept close to the common-mode reference voltage. There are several critical factors in designing CMFB circuits. The CM detector should not load the main opamp. The CM detector should have a larger input signal range. The interaction between the CMFB loop and the fully-differential signal processing should be minimized. The CMFB loop should be fast with a moderate to high gain.

## **II. MILLER COMPENSATION**

A Fully Differential Configuration as shown in Fig.1 is used to implement the two stage OTA so as to achieve better signal swing and for better noise rejection. The gain of single stage amplifier is limited to the product of the input pair transconductance and the output impedance. To achieve more gain and better signal swing at the output we can use "two-stage" opamps, with the first stage providing a high gain and the second, large swings.



Fig. 1.Miller compensated OTA



### III. DESIGN OF DIFFERENTIAL AMPLIFIER WITH CURRENT SOURCE LOAD

Transistor	Unit Transistor size (W, L) in µm	No. of fingers
M1,M2	0.5, 0.18	10
M3,M4	0.5, 0.18	20
M5	0.5, 0.18	20
M6,M8	0.5, 0.18	40
M7,M9	0.5, 0.18	20

#### Table I. W/L ratios of Two stage OTA.



Fig 2. Two stage OTA implementation in cadence

In a two stage amplifier as shown in fig.2, a compensation capacitor and a right half plane zero compensating resistor is connected between the two stages for stability during the feedback operation. The series resistor value is given by  $R_z = 1/gm_p$  where  $gm_p$  is the transconductance of the PMOS transistor of second stage. The choice of coupling capacitor is based on the phase margin required. Phase margin of 60 degrees is adequate for this design. The W/L ratios of transistors are given in Table I.

## A. AC analysis of OTA

To measure the phase margin of uncompensated OTA and compensated OTA, ac analysis was performed and the fully differential gain and phase were plotted as in fig.3. Find the unity gain frequency of the amplifier (0 dB) and find the phase at this frequency. Phase margin =  $180^{\circ}$  + phase at the unity gain frequency. Results are tabulated in Table II.





Fig.3 OTA gain and phase margin with Miller compensation capacitor and series resistor

Table II. Comparison of Phase Margins of OTA

	DC gain	DC gain	UGB	Phase margin
	( mag)	(dB)		(degrees)
Before compensation	315	50	1.885 GHz	9.5
After compensation	315	50	238.1 MHz	71.6

## B. Harmonic Distortion Analysis

The nonlinearity of a circuit can be characterized by applying a sinusoid at the input and measuring the harmonic component of the output.



Fig 4. Harmonic components at the output of the amplifier



From the fig.4, it can be observed that the odd harmonics are having higher magnitude than even harmonics. In a fully differential amplifier the even harmonics should be cancelled out.

## IV. IMPLEMENTATION OF COMMON MODE FEEDBACK CIRCUIT

The common mode voltage at the output of the OTA is detected by using split transistors of half the width as that of the transistor to which reference voltage 1.65 V is applied as shown in fig.5. The W/L ratios of the transistors are same as that of the OTA. The difference between the common mode voltage and  $V_{ref}$  is amplified by the error amplifier and output current is fed back to the gate of OTA load transistors.

#### A. CMFB CIRCUIT FOR TWO STAGE OTA

In two stage amplifier, the common mode feedback cannot be realized as for a single stage amplifier because two inversions would occur between the feedback node and the output nodes, resulting in a positive feedback that would make the system unstable. Hence the feedback was taken from the drain of split transistors itself to give negative feedback to first stage of OTA.



Fig.5 Two stage OTA symbol and common mode feedback circuit used.

# B. Common mode response for a current pulse

The common mode response of fully differential OTA with common mode feedback circuit is plotted in fig.6.Output voltage is found to be 1.653 volts.





Fig. 6 Common mode response of OTA with CMFB to a current pulse

## V. DISTORTION MEASUREMENT OF TWO-STAGE OTA WITH CMFB CIRCUIT

The total harmonic distortion of the fully differential OTA is found out. Results indicate that the even harmonics are having much less amplitude compared to the odd harmonics. Table III gives the summary of the DFT values of the fundamental and up to the fifth harmonic.

## TABLE III

Fundamental	61.15 mdB
Second harmonic	-224.2 dB
Third harmonic	-45.41 dB
Fourth harmonic	-231.7 dB
Fifth harmonic	-84.64 dB

DFT values of differential output of OTA with CMFB

#### VI. CONCLUSIONS

Fully Differential OTA is integrated with a common mode feedback circuit. Common mode feedback circuit was designed to stabilize the output common mode levels of the Operational Transconductance Amplifier. DFT of the differential output is measured and the values are plotted in dB. The even harmonics are of much less magnitude compared to odd harmonics because of fully differential configuration.

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## BIOGRAPHY



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