Comparative Implementation of Cryptographic Algorithms on ARM Platform

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Abstract: This paper presents the comparison between two cryptographic algorithms AES and Blowfish algorithm on the basis of ARM implementation. LPC 2148 from NXP Philips family kit is used for implementation. In Embedded system security, Blowfish is suitable. For comparison, we considered points like memory size, encryption cycle, and decryption cycle for both algorithms on ARM7 etc. For small embedded system like mobile, smart card etc Blowfish is best algorithm for security.

Keywords: Encryption, Decryption, AES, Blowfish

I. INTRODUCTION

CRYPTOGRAPHY algorithms are divided into Symmetric and Asymmetric key cryptography [1]. Symmetric key encryption use only key to encrypt and decrypt data. Key plays an important role in encryption and decryption. If a weak key is used in the algorithm then easily data can be decrypted. The size of the key determines the strength of Symmetric key encryption. Symmetric algorithms are of two types: block ciphers and stream ciphers. The block ciphers are operating on data in groups or blocks. Examples are Data Encryption Standard (DES), Advanced Encryption Standard (AES) and Blowfish. Stream ciphers are operating on a single bit at a time. RC4 is a stream cipher algorithm. In Asymmetric key encryption, two keys are used: private keys and public keys. Public key is used for encryption and private key is used for decryption (e.g. Digital Signatures). Public key is known to the public and private key is known only to the user. Plain Text is the original message that we wish to communicate with the others is defined as Plain Text. Cipher Text is the message which has been converted by the encryption algorithm is called cipher text. In cryptography the original message is transformed into non-readable message. Encryption is a process of converting plain text into cipher text is called as Encryption. Cryptography uses the encryption algorithm and a key to send confidential data through an insecure channel. Decryption is a reverse process of encryption is called decryption. It is a process of converting cipher text into Plain Text. Decryption requires decryption algorithm and a key. Cryptography provides a number of security goals to ensure the privacy of data, non-alteration of data cryptography it is widely used today due to the great security advantages of it. Here are the various goals of cryptography.

Confidentiality: Information in computer is transmitted and has to be accessed only by the authorized party and not by anyone else.

Authentication: The information received by any system has to check the identity of the sender that whether the Information is arriving from a authorized person or a false identity.

Integrity: Only the authorized party is allowed to modify the transmitted information. No one in between the sender and receiver are allowed to alter the given message.

Our PDAs store personal e-mail and contact lists; GPS receivers and, soon, cell phones keep logs of our movements; and our automobiles record our driving habits. On top of that, users demand products that can be reprogrammed during normal use, enabling them to eliminate bugs and add new features as firmware upgrades become available.
II. RELATED WORK:

In paper [5] “Superiority of Blowfish Algorithm” in this paper comparison takes place on java. Comparison between four most common and used symmetric key algorithms: DES, 3DES, AES and Blowfish. A comparison has been made on the basis of these parameters: rounds block size, key size, and encryption / decryption time, CPU process time in the form of throughput and power consumption. These results show that blowfish is better than other algorithm.

In paper [7] comparison takes place in symmetric cryptographic algorithm by taking software and hardware support.

In paper [6] they said that AES is faster and more efficient than other encryption algorithms. When the transmission of data is considered there is insignificant difference in performance of different symmetric key schemes. Under the scenario of data transfer it would be better to use AES scheme in case the encrypted data is stored at the other end and decrypted multiple times.

In paper [4] consider the performance of encryption algorithm for text files. AES, DES and RSA algorithm has been evaluated from the parameters like Computation time, Memory usage, Output bytes. Comparing these three algorithms they found RSA takes more time for computation process. The memory usage of each algorithm is considered as memory byte level. RSA takes more memory than AES and DES. Finally, the output byte is calculated by the size of output byte of each algorithm. The level of output byte is equal for AES and DES, but RSA algorithm produces low level of output byte.

III. PROPOSED WORK

This paper presents comparison between AES and BLOWFISH algorithms on cryptography. Here we used 32 bit ARM LPC 2148 platform to implement these algorithms. Comparison taken place on various points like key length, block size, encryption time and decryption time, simplicity, security. Proposed block diagram as shown in figure 1.

We considered two algorithms separately. Text and key given to ARM kit by PC serially. We create simple GUI on PC using visual basic 6. Code is written in embedded c for both algorithms. Keil 4.0 compiler used to run programs. Then the encrypted text displayed on LCD.

A. Need of ARM implementation:

A software implementation of a cryptography scheme provides the benefits of flexibility, speed of implementation, and lower cost over time. More importantly, the NXP ARM microcontrollers feature. In Application Programming (IAP) and the popular LPC2300 and LPC2400 series also feature Ethernet, USB and CANIAP allows customers to periodically change the security algorithm in the field whether or not the product has been comprised. Competitive hardware encryption cannot be updated without replacing the microcontroller, which is costly and complicated [8].

![Fig 1. Block Diagram](image_url)
B. **ARM kit description:**

The LPC2148 microcontrollers are based on a 32/16 bit ARM7TDMI-S CPU with real-time emulation and embedded trace support, that combines the microcontroller with embedded high speed flash memory ranging from 32 kB to 512 kB. A 128-bit wide memory interface and a unique accelerator architecture enable 32-bit code execution at the maximum clock rate. For critical code size applications, the alternative 16-bit Thumb mode reduces code by more than 30% with minimal performance penalty. Due to their tiny size and low power consumption, LPC2148 are ideal for applications where miniaturization is a key requirement, such as access control and point-of-sale. A blend of serial communications interfaces ranging from a USB 2.0 Full Speed device, multiple UARTs, SPI, SSP to I2Cs, and on-chip SRAM of 8 kB up to 40 kB, make these devices very well suited for communication gateways and protocol converters, soft modems, voice recognition and low end imaging, providing both large buffer size and high processing power. Various 32-bit timers, single or dual 10-bit ADC(s), 10-bit DAC, PWM channels and 45 fast GPIO lines with up to nine edge or level sensitive external interrupt pins make these microcontrollers particularly suitable for industrial control and medical systems.

C. **Algorithm description**

1. **AES**

(Advanced Encryption Standard), also known as the Rijndael (pronounced as Rain Doll) algorithm, is a symmetric block cipher that can encrypt data blocks of 128 bits using symmetric keys 128, 192, or 256. AES was introduced to replace the DES. Brute force attack is the only effective attack known against this algorithm. The 128 bit data block is divided into 16 bytes, which are represented by a 4X4 matrix of bytes. The entries are denoted by,

\[
S_{0,0}, S_{0,1}, S_{0,2}, S_{0,3}, \\
S_{1,0}, S_{1,1}, S_{1,2}, S_{1,3}, \\
S_{2,0}, S_{2,1}, S_{2,2}, S_{2,3}, \\
S_{3,0}, S_{3,1}, S_{3,2}, S_{3,3}
\]

The matrix represents a state \( S \). All the four transformations map an input state to an output state. The AddRoundKey involves only one bit-wise XOR operation between the state \( S \) and the round key. The shiftRows cyclically shifts \( k \) bytes to the left on \( k \)th row of the state matrix, \( k=0\sim3 \). The position changes to,

\[
s0.0 \ s0.1 \ s0.2 \ s0.3 \\
s1.1 \ s1.2 \ s1.3 \ s1.0 \\
s2.2 \ s2.3 \ s2.0 \ s2.1 \\
s3.3 \ s3.0 \ s3.1 \ s3.2
\]

The MixColumn uses each column of the state matrix as a polynomial over GF(2^8) and multiples them modulo \( x+1 \) with a polynomial \( a(x) = \{03\}x + \{01\}x + \{01\}x + \{02\} \).
2. **BLOWFISH:**

*Blowfish* is a symmetric block cipher that can be effectively used for encryption and safeguarding of data. It takes a variable-length key, from 32 bits to 448 bits, making it ideal for securing data. *Blowfish* was designed in 1993 by [Bruce Schneier](https://www.schneier.com) as a fast, free alternative to existing encryption algorithms. *Blowfish* is unpatented and license-free, and is available free for all uses. *Blowfish Algorithm* is a Feistel Network, iterating a simple encryption function 16 times. The block size is 64 bits, and the key can be any length up to 448 bits. Although there is a complex initialization phase required before any encryption can take place, the actual encryption of data is very efficient on large microprocessors. *Blowfish* is a variable-length key block cipher. It is suitable for applications where the key does not change often, like a communications link or an automatic file encryptor. A graphical representation of the Blowfish algorithm appears in Figure 3. In this description, a 64-bit plaintext message is first divided into 32 bits. The "left" 32 bits are XORed with the first element of a P-array to create a value.
we'll call P', run through a transformation function called F, then XORed with the "right" 32 bits of the message to produce a new value I'll call F'. F' then replaces the "left" half of the message and P' replaces the "right" half, and the process is repeated 15 more times with successive members of the P-array. The resulting P' and F' are then XORed with the last two entries in the P-array (entries 17 and 18), and recombined to produce the 64-bit ciphertext.

IV. EXPERIMENTAL RESULT

![ARM LPC 2148 Kit With LCD](image)

**Fig 5. ARM LPC 2148 Kit With LCD**

A. Practical analysis

<table>
<thead>
<tr>
<th>ALGORITHM</th>
<th>AES</th>
<th>BLOWFISH</th>
</tr>
</thead>
<tbody>
<tr>
<td>BLOCK SIZE IN BIT</td>
<td>128</td>
<td>64</td>
</tr>
<tr>
<td>KEY SIZE IN BIT</td>
<td>64</td>
<td>64 TO 448</td>
</tr>
<tr>
<td>MEMORY SIZE IN BYTE</td>
<td>5966</td>
<td>4011</td>
</tr>
<tr>
<td>ENCRYPTION TIME(cycle)</td>
<td>639</td>
<td>440</td>
</tr>
<tr>
<td>DECRYPTION TIME(cycle)</td>
<td>638</td>
<td>443</td>
</tr>
</tbody>
</table>

B. Graphical presentation:

![Graph](image)

From graph and table, it is clear that AES algorithm and BLOWFISH implementation on ARM LPC2148 are possible. Both are working smoothly on 32 bit platform separately. When we measure encryption time, decryption time it is observed that ARS require more cycles than BLOWFISH.
V. CONCLUSION

From results, it is shown that, blowfish algorithm requires less time to perform, so in that case blowfish is good. As the algorithm steps consider AES is more complex algorithm than BLOWFISH, it is good for security and key size is fixed for AES as blowfish have variable key size. Memory requirement is less in BLOWFISH. For small embedded system like mobile, smart card etc Blowfish is best algorithm for security.

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BIOGRAPHY

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