



Performance Analysis of Modular Multilevel Converter (MMC) with Continuous and Discontinuous Pulse Width Modulation (PWM)

Manish V. Kurwale¹, Palak G. Sharma², Gautam Bacher³

Lecturer, Dept. of Electrical Engineering, RGCOER, Nagpur, Maharashtra, India¹

Lecturer, Dept. of Electrical Engineering, RGCOER, Nagpur, Maharashtra, India²

Lecturer, Department of Electrical, Electronics & Instrumentation Engineering, BITS Pilani, Goa Campus, Goa, India³

ABSTRACT: Modular structured multilevel inverter is very attractive in high voltage and high power applications. This inverter has a few advantages. First, by using a multilevel structure, the stress on each switching device can be reduced MMC can handle higher voltage without using an expensive and bulky step-up transformer in various application. This paper offers a performance comparison of various multicarrier continuous and discontinuous pulse width modulations (PWM) Techniques for the control of the modular multilevel converter (MMC). Selection of the carrier frequency and the multi-carrier PWM method is an important design question for the MMC topology. The paper makes an effort to apply some continuous and discontinuous modulating of MMC this comprehensive simulation has been done in MATLAB.

Keywords: Modular converter, continuous and discontinuous pulse width modulations (PWM) Techniques.

1. INTRODUCTION

The progress of high-power high-voltage power electronic equipment for various medium- to high-voltage applications continues to benefit from the advancement of the high-power fully-controlled semiconductors [1]. Applications for such equipment include motor drives, traction systems [1], high voltage direct current (HVDC) power transmission [2], flexible alternating current transmission systems (FACTS) such as static compensators (STATCOMs) to mention just a few [3]. As the level of power and voltage needs to be increased for many applications, the rapid development of multilevel voltage source converters (VSCs) over the last three decades has resulted, for instance, in many successful commercially viable HVDC systems [2]. In multilevel converters, the staircase approximation of the sinusoidal waveform with numerous DC sources is used, effectively reducing the total harmonic distortion (THD) of the output voltage [1-2]. The MMC [11-14] interesting multilevel converter providing various advantages, such as modular construction, multilevel waveform with a large number of steps and fault management [12]. An MMC can be configured using half- or full-bridge circuits and their associated capacitors. The use of a number of full-bridge sub-modules cascaded to create phase-legs result in an AC/AC converter [13]. Similarly the uses of half-bridge sub-modules, or DC-choppers, cascaded in phase-legs are a DC/AC converter. The latter converter topology can also operate in a back-to-back configuration for HVDC power transmission or motor control, without the need of a large DC-side capacitor. This reduces the cost of the converter and increases its reliability. However, limited information is available in the open technical literature about the performance of many well-known PWM techniques [8] to control the converter operation. Moreover, it is important to report the effects of the control method to a number of variables such as: the synthesis of the voltage waveform, the charging and discharging of the floating capacitors, the total harmonic distortion and associated switching losses

International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering

(An ISO 3297: 2007 Certified Organization)

Vol. 3, Issue 2, February 2014

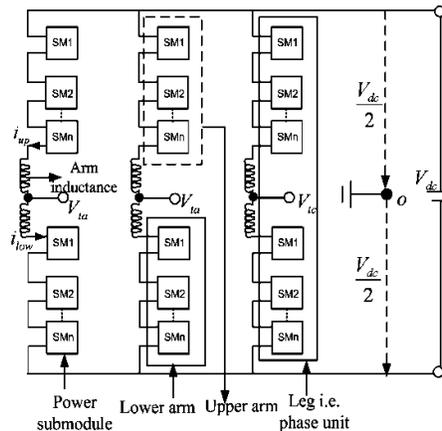


Fig 1. Topology of three phase MMC

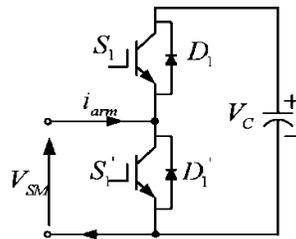


Fig.2. Structure of sub module (SM)

The objective of this paper is to contribute to the Understanding of the operation and performance of the MMC Topology under Continuous and Discontinuous PWM techniques. PWM applies a pulse train of fixed amplitude and frequency only the width is varied in proportion to an input voltage In PWM technique the power semiconductor switches are turned ON and turn OFF several times during HALF cycles and output voltage is controlled by changing the width pulse.

II. LITERATURE SURVEY

The idea behind doing this project is taken from different research papers presented in various conferences and IEEE transactions. I had gone through a number of research papers to obtain the basic knowledge about this research. I have taken different innovative idea from all these sources and I have applied those in research paper.

The multilevel inverters have drawn tremendous interest in the power industry. Modular structured multilevel inverter is very attractive in high voltage and high power application. It may be easier to produce a high voltage, high power inverter with the multilevel structure because of the way in which device voltage stresses are controlled in the structure [1]. By using multilevel structure, the stress on each switching device can be reduced proportional to the number of levels of the multilevel inverter. Thus, the inverter can handle higher voltage without using an expensive and bulky step-up transformer in various applications. As the number of inverter output voltage levels is increased, harmonics content of the output voltage waveform decreases significantly enough to avoid the need of bulky filters [7,9,10]. The multilevel inverter has drawn tremendous interest in the power industry. This inverter is very attractive in high-voltage and high-power applications. This inverter has a few advantages [16]. It may be easier to produce a high-power and high-voltage inverter with the multilevel structure because of the way in which device voltage stresses are controlled in the structure. Increasing the number of voltage levels in the inverter without requiring higher ratings on individual devices can increase the power rating [11-13].



International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering

(An ISO 3297: 2007 Certified Organization)

Vol. 3, Issue 2, February 2014

The stress on each switching device can be reduced proportional to the number of levels of the multilevel inverter, thus the inverter can handle higher voltage without using an expensive and bulky step-up transformer in various application. As the number of voltage levels increases, the harmonic content of the output voltage waveform decreases significantly enough to avoid the need of bulky filters [2,6]. The topological structure of multilevel inverter must have less switching devices as far as possible, be capable of withstanding very high input voltage for high-power application and have lower switching frequency for each switching device [16].

III. MODULAR MULTILEVEL CONVERTERS

Fig.1 shows the three-phase MMC with n sub-module in each arm. There are two arms in each phase, which are positive-arm and negative arm. The no. of levels in the output of the phase – leg depends not only on the no. of sub-modules in the arm of the topology but also on the way the converter leg depends not only on the no. of sub-modules in the arm of the topology but also on the way the converter leg is modulated. Output line-to-neutral voltage has $n+1$ levels if topology consisting of ‘ n ’ sub-modules per arm ($2n$ per phase leg). The no. of sub-modules that are continuously connected in the whole phase leg remains constant and equal to n , meaning that n -upper + n -lower = n (where n -upper and n -lower are the sub-modules connected in the upper and lower arm respectively). When carrier phase-shifted SPWM is adopted, $2n$ sub-modules in each phase leg have $2n$ triangular waveforms with the same frequency but a phase difference of $3600/2n$, thus output line-to-neutral voltage has $2n+1$ levels. Fig. 2 is a single SM structure. The SM output voltage V_{SM} has two values; $V_{SM} = V_C$, when the upper switch is on and the lower one is off; or $V_{SM} = 0$ when the lower switch is on and the upper switch is off. This means each SM has only two states in normal operations: switched on or switched off. Output voltage and the states of charge on DC-side capacitor are listed in table 1.

Each of the arms of the topology can be seen as a voltage source, and the voltage of the arm is given by eq. [16].

$$V_{arm} = \sum_{i=1}^n S_{armi} V_{SMarmi} + L \frac{di_{arm}}{dt}$$

‘SM’ is OFF when $S1=0, S2=1$, ‘SM’ is ON when $S1=1, S2=0$

TABLE I
 OUTPUT AND CHARGING STATE OF A SUB-MODULE

S1	S1'	D1	D1'	Current direction	Capacitor state	Output voltage
OFF	ON	OFF	OFF	$i_{arm} > 0$	Unchanged	0
OFF	OFF	OFF	ON	$i_{arm} < 0$	Unchanged	0
OFF	OFF	ON	OFF	$i_{arm} > 0$	Charging	v_c
ON	OFF	OFF	OFF	$i_{arm} < 0$	Discharging	v_c

IV. MULTICARRIER CONTINUOUS AND DISCONTINUOUS PWM TECHNIQUES.

PWM applies a pulse train of fixed amplitude and frequency only the width is varied in proportion to an input voltage. In PWM technique the power semiconductor switches are turned ON and turn OFF several times during HALF cycles and output voltage is controlled by changing the width pulse. $N =$ number of carriers ($n-1$), Modulation index = $A_m/N \cdot A_c$ Where $N = (n-1)/2$, Where $n =$ number of levels. Frequency of reference wave is 50 Hz and frequency of carrier is 2kHz. We made comparing of reference wave and carrier wave its result is pure PWM for switching of switches for 3 level no. carriers requires two and for 5 level no. of carrier requires 4 upper two and lowers two. By using this technique we are giving the switching pulse to switches. The multicarrier PWM techniques are based on a single Modulating or reference signal, which in most cases is Sinusoidal. This reference waveform is compared and



International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering

(An ISO 3297: 2007 Certified Organization)

Vol. 3, Issue 2, February 2014

sampled through a number of triangular waveforms and for this reason the PWM techniques considered here are called multicarrier PWM techniques. They can be categorized as follows:

A. Phase disposition (PD) [7]

In the phase-disposition method all triangular carriers are in Phase the significant line-to-line voltage harmonics are presented as sidebands around the carrier frequency as the theory suggests.

B. Phase opposition disposition (POD) [7]

In POD, the positive and negative carriers are in phase, but between them, there is a phase shift of 180 degrees as shown in Fig. 4(b). Significant harmonics are presented, similarly to PD, as sidebands around the carrier frequency.

C. Alternative phase opposition disposition (APOD) [7]

In APOD, carrier signals are alternately displaced 180Degrees between them as shown in Fig.4(c). Similarly to the Previous two carrier disposition methods, sidebands to theCarrier frequency is presented as the most significant harmonics.The PD, POD and APOD control methods have theProperty of producing signals with significantly lower Switching frequency than the carrier frequency. The linear or continuous modulating functions are co-sinusoidalmodulating functions (CMF), Harmonics modulating function (HMF), and Trapezoidal modulating function (TMF) and non-linear or discontinuous modulating functions are Non-linear modulating function NLMF-1, NLMF-2, NLMF-3. [15]

$$\text{CMF: } F1(\alpha) = \frac{1}{2} \cos(\alpha)$$

$$\text{HMF: } F1(\alpha) = F1(\alpha) = \frac{1}{\sqrt{3}} [\cos(\alpha) - \frac{1}{6} \cos(3\alpha)]$$

TMF:

$$F1(\alpha) = \begin{cases} 0.5 & \text{if } -60^\circ \leq \alpha < 60^\circ \\ 1.5 - \frac{\alpha}{60^\circ} & \text{if } 60^\circ \leq \alpha < 120^\circ \\ -0.5 & \text{if } 120^\circ \leq \alpha < 240^\circ \\ \frac{\alpha}{60^\circ} - 4.5 & \text{if } 240^\circ \leq \alpha < 300^\circ \end{cases}$$

LMF-1:

F1(m1α) =

$$\begin{cases} 0.5 & \text{if } -30^\circ \leq \alpha < 30^\circ \\ m\cos(\alpha - 30^\circ) - 0.5 & \text{if } 30^\circ \leq \alpha < 90^\circ \\ m\cos(\alpha + 30^\circ) + 0.5 & \text{if } 90^\circ \leq \alpha < 150^\circ \\ -0.5 & \text{if } 150^\circ \leq \alpha < 210^\circ \\ m\cos(\alpha - 30^\circ) + 0.5 & \text{if } 210^\circ \leq \alpha < 270^\circ \\ m\cos(\alpha + 30^\circ) - 0.5 & \text{if } 270^\circ \leq \alpha < 330^\circ \end{cases}$$



International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering

(An ISO 3297: 2007 Certified Organization)

Vol. 3, Issue 2, February 2014

NLMF-2:

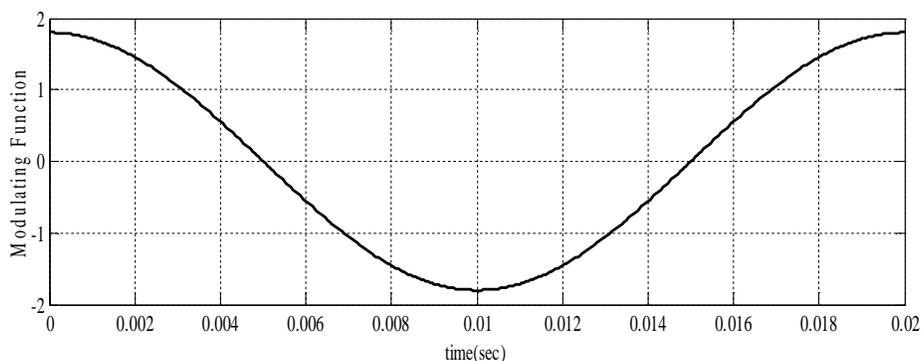
$F1(m1\alpha) =$

$$\left\{ \begin{array}{ll} m\cos(\alpha-30^\circ) - 0.5 & \text{if } 0^\circ \leq \alpha < 30^\circ \text{ or } 90^\circ \leq \alpha < 120^\circ \\ 0.5 & \text{if } 30^\circ \leq \alpha < 60^\circ \text{ or } 300^\circ \leq \alpha < 330^\circ \\ m\cos(\alpha+30^\circ) + 0.5 & \text{if } 60^\circ \leq \alpha < 90^\circ \text{ or } 150^\circ \leq \alpha < 180^\circ \\ -0.5 & \text{if } 120^\circ \leq \alpha < 150^\circ \text{ or } 210^\circ \leq \alpha < 240^\circ \\ m\cos(\alpha-30^\circ) + 0.5 & \text{if } 180^\circ \leq \alpha < 210^\circ \text{ or } 270^\circ \leq \alpha < 300^\circ \\ m\cos(\alpha+30^\circ) - 0.5 & \text{if } 240^\circ \leq \alpha < 270^\circ \text{ or } 330^\circ \leq \alpha < 360^\circ \end{array} \right.$$

NLMF-3:

$F1(m1\alpha) =$

$$\left\{ \begin{array}{ll} m \left[\cos(\alpha-30^\circ) - \frac{2.25}{\pi} \right] + 0.25 & \text{if } 0^\circ \leq \alpha < 120^\circ \\ -0.5 & \text{if } 120^\circ \leq \alpha < 240^\circ \\ m \left[\cos(\alpha+30^\circ) - \frac{2.25}{\pi} \right] + 0.25 & \text{if } 240^\circ \leq \alpha < 360^\circ \end{array} \right.$$



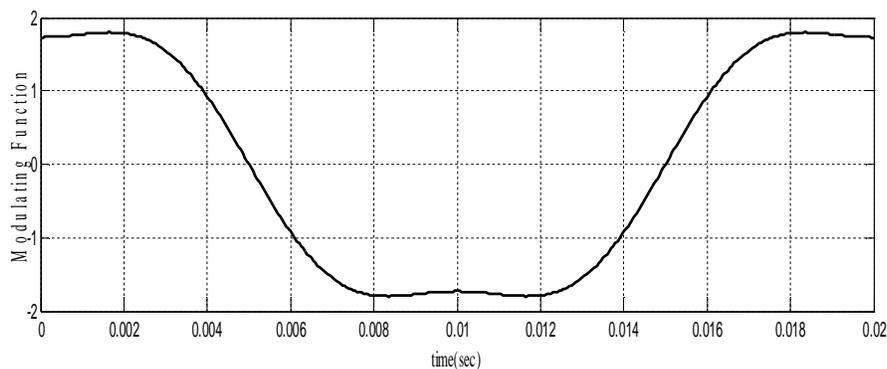
(a)Co-sinusoidal modulating function



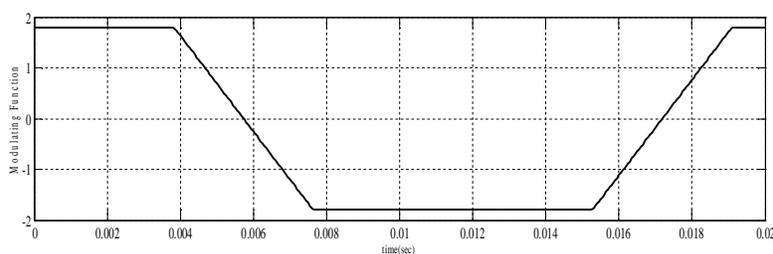
International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering

(An ISO 3297: 2007 Certified Organization)

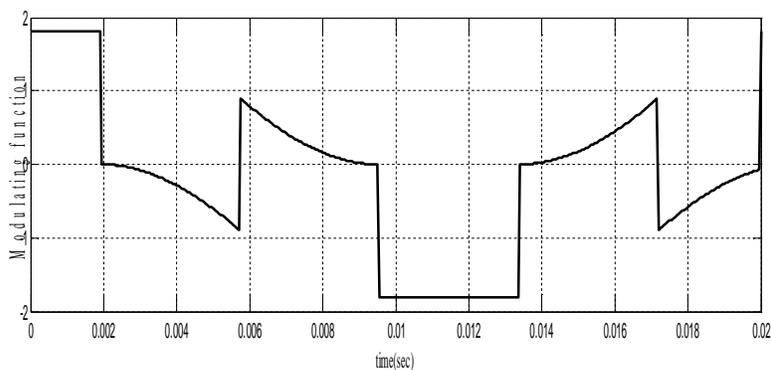
Vol. 3, Issue 2, February 2014



(b) Harmonic modulating function



(c) Trapezoidal modulating function



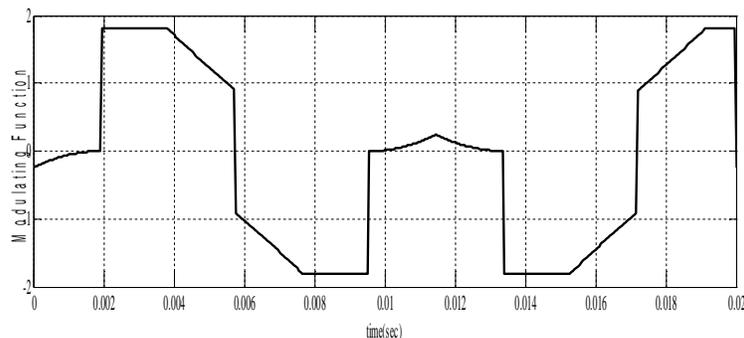
(d) Nonlinear modulating function-1



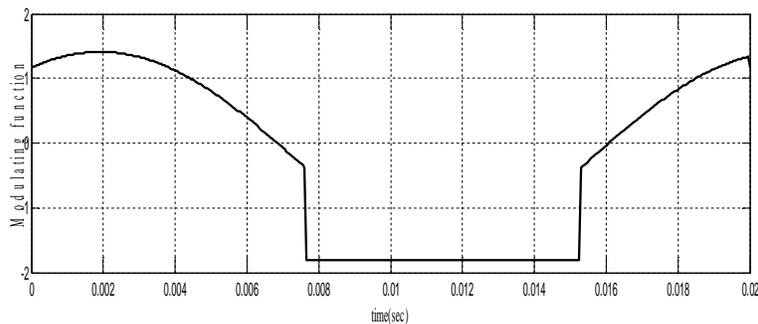
International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering

(An ISO 3297: 2007 Certified Organization)

Vol. 3, Issue 2, February 2014

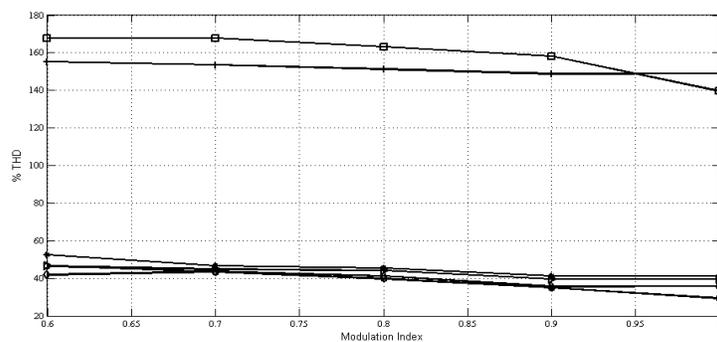


(e) Nonlinear modulating function-2



(f) Nonlinear modulating function-3

V. SIMULATION RESULTS



(a) APOD (THD)

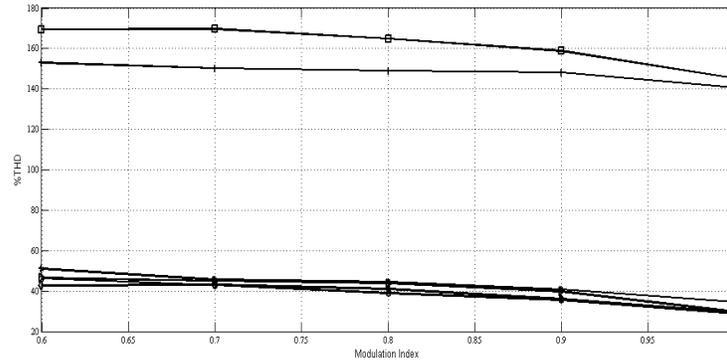
The above results shows the percentage of THD of MMC versus different modulation index for different continuous and discontinuous waveforms and Alternate Phase opposite disposition carrier waveform, where all carriers are alternatively in opposition, each carrier is phase shifted by 180° from its adjacent carrier.



International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering

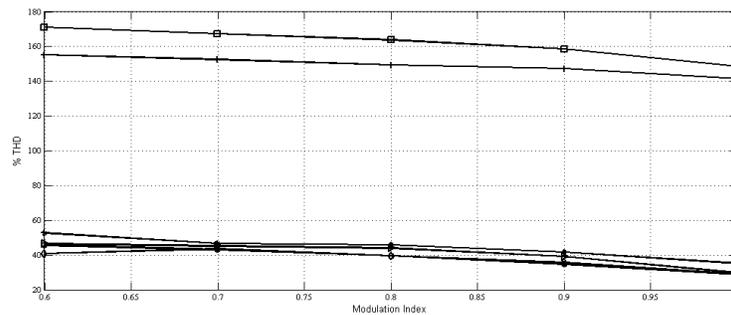
(An ISO 3297: 2007 Certified Organization)

Vol. 3, Issue 2, February 2014



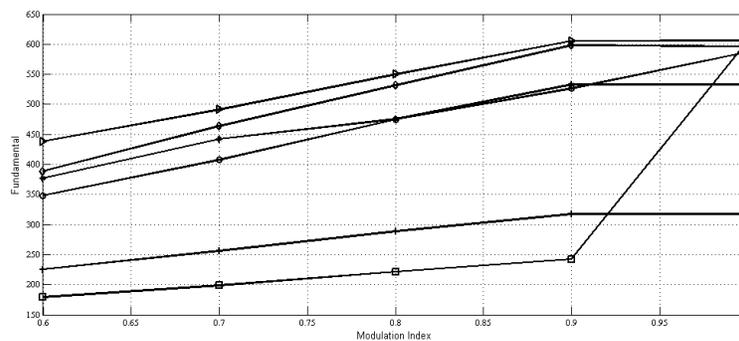
(b)PD (THD)

The above results shows the percentage of THD of MMC verses different modulation index for different continuous and discontinues waveforms and Phase disposition carrier waveform, where all carriers are in phase.



(c)POD (THD)

The above results shows the percentage of THD of MMC verses different modulation index for different continuous and discontinues waveforms and Phase opposite disposition carrier waveform, where all carriers above the zero value reference are in phase among them but in opposition with those below.



(d) APOD (Fundamental)

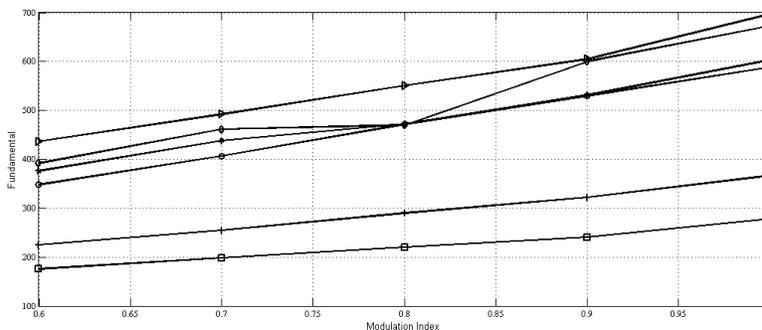


International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering

(An ISO 3297: 2007 Certified Organization)

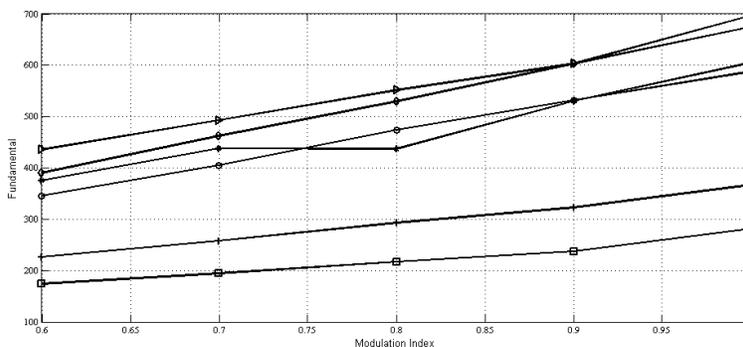
Vol. 3, Issue 2, February 2014

The above results shows the fundamental value reference wave of MMC versus different modulation index for different continuous and discontinues waveforms and Alternate Phase opposite disposition carrier waveform



(e) PD (Fundamental)

The above results shows the fundamental value reference wave of MMC versus different modulation index for different continuous and discontinues waveforms and Phase disposition carrier waveform.



(f)POD (Fundamental)

The above results show the fundamental value reference wave of MMC versus different modulation index for different continuous and discontinues waveforms and Phase opposite disposition carrier waveform.

Different graph of THD versus Different Modulation index shown in figures. Fig 4(a) shows the characteristics of APOD in terms of THD versus Modulation index. . Fig 4(b) shows the characteristics of PD in terms of THD versus Modulation index. . Fig 4(c) shows the characteristics of POD in terms of THD versus Modulation index. . Fig 4(d) shows the characteristics of APOD in terms of Fundamental versus Modulation index. . Fig 4(e) shows the characteristics of PD in terms of Fundamental versus Modulation index. . Fig 4(f) shows the characteristics of POD in terms of Fundamental versus Modulation index. Where CMF is represented by '○', HMF is represented by '◇' Where TMF is represented by '▷' Where NLMF-1, NLMF-2, NLMF-3, is represented by '□', '+', '*'.



International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering

(An ISO 3297: 2007 Certified Organization)

Vol. 3, Issue 2, February 2014

Table II

Alternate Phase Opposite Disposition										
Modulation Index	0.6		0.7		0.8		0.9		1	
	TH D %	Fundamental								
CMF	46.63	348	43.37	407.3	39.75	474.8	35.01	526.2	29.30	586.8
HMF	41.85	388.8	43.53	463.7	41.25	531.3	35.63	598.1	35.63	598.1
TMF	46.44	438.1	44.78	491	44.05	550	39.52	605.6	39.52	605.6
NLMF-1	167.81	178.9	167.70	198.6	163.14	221.2	158.09	242.3	139.52	607.6
NLMF-2	155.15	225.7	153.56	256.2	151.21	288.8	148.85	317.3	148.85	317.3
NLMF-3	52.45	377.1	46.61	441.9	95.41	475.5	41.19	532.8	41.19	532.8
Phase Disposition										
Modulation Index	0.6		0.7		0.8		0.9		1	
	TH D %	Fundamental								
CMF	45.57	347.7	43	407	39.54	471.6	34.81	529.5	28.69	588
HMF	40.87	391.9	43.40	461.2	39.63	470.2	35.62	600.4	28.84	673
TMF	46.42	436.8	45.14	492.1	44	550.9	39.16	604.7	29.67	697
NLMF-1	171.17	175.3	167.34	198.4	163.84	220.2	158.51	240.3	148.63	278
NLMF-2	155.15	225	152.61	254.6	149.49	289.6	147.35	322.3	141.46	366.7
NLMF-3	52.60	376	46.54	437.8	45.72	472.1	41.65	531	35.12	603
Phase Opposite Disposition										
Modulation Index	0.6		0.7		0.8		0.9		1	
	TH D %	Fundamental								
CMF	46.11	345.5	42.94	405.2	38.87	473.8	35.59	532	28.76	587.6
HMF	42.74	390.4	43.03	462.1	41.01	529.6	36	603.3	29.28	675
TMF	46.33	435.4	44.94	492.8	43.64	551.2	39.64	603.4	29.65	697
NLMF-1	169.46	174.3	169.64	194.2	164.80	217	159.82	237.7	145.27	281.8
NLMF-2	153	226.5	150.18	257.9	149.06	292.9	148.07	322.8	140.59	367.1
NLMF-3	50.98	375.2	45.56	438.3	44.58	473.1	40.83	530.7	34.56	604.8

HARMONICS ANALYSIS OF APOD, PD AND POD WITH CONTINUOUS AND DISCONTINUOUS MULTICARRIER WAVEFORM



International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering

(An ISO 3297: 2007 Certified Organization)

Vol. 3, Issue 2, February 2014

VI.CONCLUSION

The modular multilevel converter and different multi-carrier continuous and discontinuous PWM techniques. Have been discussed in this paper. THD and harmonic spectrum, from simulations with the MATLAB software have been reported. Comparison of the typical multicarrier continuous and discontinuous PWM techniques. the simulation results which are shown in fig IV(a) to IV(d) and Table II shows that multi-carrier discontinuous PWM methods gives more harmonics distortion in the output voltage because it does not cut often to carrier signal, while continuous modulation methods gives less distortion in the output voltage because the reference waveform is continuous and cuts to more points in the carrier waveform. The distortion in the output voltage is given without filter. Discontinuous modulation though gives high distortion, it gives low switching losses. Simulation results acknowledge the above opinions.

REFERENCES

- [1] J. Rodriguez, J.S. Lai, F.Z. Peng, "Multilevel inverters: A survey of topologies, controls, and applications," IEEE Trans. Ind. Electron., Vol. 49, pp.724-738, August 2002.
- [2] N. Flourentzou, V.G. Agelidis, G.D. Demetriades, "VSC-based HVDC power transmission systems: an overview," IEEE Trans. Power Electr., Vol.24, No.2, pp. 1-11, 2009.
- [3] E. Acha, V.G. Agelidis, O. Anaya-Lara, T.J.E. Miller, "Power Electronic control in electric systems", Butterworth-Heinemann, ISBN 0750651261, January 2002.
- [4] A. Nabae, I. Takahashi, and H. Akagi, "A neutral-point-clamped PWM inverter," IEEE Trans. Ind. Appl., Vol. IA-17, pp. 518-523, 1981.
- [5] Y. Cheng et al., "A comparison of diode-clamped and cascaded multilevel converters for STATCOM with energy storage," IEEE Trans., on Industrial Electronics, Vol. 53, no. 5, Oct 2006, pp. 1512-1521.
- [6] T.A. Maynard and H. Foch, "Multilevel conversion: high voltage choppers and voltage source inverters," in Proc. of IEEE Power Electron. Spec. Conf., 1992, Vol. 1, pp. 397-403.
- [7] G. Carrara et al., "A new multilevel PWM method: A theoretical Analysis," IEEE Trans Power Electronics, Vol. 7, No. 3, July 1992, pp. 497-505.
- [8] Shoji Fukuda and Kunio Suzuki, "harmonic evaluations of carrier based pwm methods using harmonics distortion determining factor", IEEE Trans. Power Electr., Vol. 24, No.2, pp. 1-11, 2009.
- [9] Georgios S. Konstantinou, Vassilios G. Agelidis, "Performance Evaluation Of Half-Bridge Cascaded Multilevel Converters Operated with Multicarrier Sinusoidal PWM Techniques" 2009 IEEE ICIEA conference-july 2009, pp.3399-3404
- [10] M. Hagiwara and H. Akagi, "PWM Control and Experiment on Modular Multilevel Converters," IEE Power Electronic Specialist Conference Rhodes, 15-19 June 2008, pp. 154-16.
- [11] Li Qiang, He Zhiyuan, Tang Guangfu, "Investigation of the Harmonic Optimization Approaches in the New Modular Multilevel Converters" IEEE transaction on industrial application 5, september/october-2010. vol.no-35,.
- [12] Ke Li, and Chengyong Zhao, "New Technologies of Modular Multilevel Converter for VSC-HVDC Application" IEEE 1st Power Electronic & Drive Systems & Technologies Conference- June 2010 pp.378-385.
- [13] H. Mohammadi Pirouz, & M. Tavakoli Bina, "New Transformer less STATCOM Topology for Compensating Unbalanced Medium-Voltage Loads" 1st Power Electronic & Drive Systems & Technologies Conference- Jan 2003, pp. 1307-1312.
- [14] Grain. P. Adam, O. Anaya-Lara, G. Burt, J. McDonald, "Transformer+less STATCOM based on a five-level modular multilevel converter" IEEE Trans. Industrial Electronics, Vol. 52, No. 1, February 2005, Pp.190- 196.
- [15] Andrzej M. Trzynadowski, "An Overview of Modern PWM Techniques for Three-Phase, voltage-controlled, voltage source Inverters" in Proc. of the IEEE International Symposium on Industrial Electronics, (ISIE) '96, 17-20 June 1996 pp.25-31
- [16] G. P. adam, o. Anaya_Lara, G.M. Burt, D. Telford, B.W. Williams and J. R. McDonald, "modular multilevel inverter; pulse width modulation and capacitor balancing technique," IET Power Electronics, 2010, vol.3 Issue 5, pp. 702-715.



ISSN (Print) : 2320 – 3765
ISSN (Online): 2278 – 8875

International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering

(An ISO 3297: 2007 Certified Organization)

Vol. 3, Issue 2, February 2014

BIOGRAPHY



Mr. Manish V. Kurwale received his Bachelor's Degree in Electrical Engineering from R.T.M.N.U Nagpur in 2008 & Master's Degree from R.T.M.N.U in 2012. Presently he is working in RGCER, Nagpur as a Lecturer in Department of Electrical Engineering.



Ms. Palak.G. Sharma received her bachelor's degree in electrical engineering from R.T.M.N.U Nagpur in 2011 & Master's Degree from RCOEM, Nagpur in 2013. Presently she is working in RGCER, Nagpur as a Lecturer in Department of Electrical Engineering.



Mr. Gautam Bacher received his Bachelor's Degree in Electrical Engineering from R.T.M.N.U Nagpur & Master's Degree from NIT Kurukshetra. Presently he is pursuing his Phd from BITS-Pilani, Rajasthan. He is working in BITS Pilani, K.K.Birla Goa Campus ,as a Lecturer in Department of Electrical, Electronics & Instrumentation Engineering.