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Performance Investigation of Digitally Controlled Central Three-Level Diode Clamped Inverter in Two-Stage Solar Photo Voltaic (SPV) System

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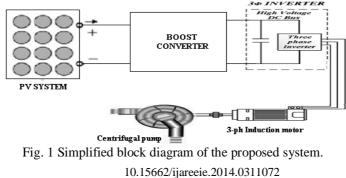
ABSTRACT: A novel converter is proposed in this paper for photovoltaic (PV) water pumping applications and this converter drives the three-phase induction motor by utilizing PV energy. By replacing the commercial dc motor with IM, it saves the cost, less maintenance, more efficient as well as it presents better performance for water pumping applications. The developed system in this paper is based on boost converter and a full-bridge three-phase three level inverter. The topology of the boost converter has low input current ripple and high voltage gain. This paper presents simulation of open loop and closed loop controlled boost converter system for solar installation system. Matlab models for open loop and closed loop systems are developed using the blocks of simulink and the same are used for simulation studies. The closed loop system is able to maintain constant voltage. This converter has advantages like reduced hardware and good output voltage regulation. Thus the boost converter is capable of improving the voltage level from 15 V to the required level.

KEYWORDS: Photovoltaic (PV) system, solar water pump set, dc–dc Boost Converter, MPPT, Diode Clamped Inverter, SVPWM, FPGA, LC filter, PI Controller Three phase induction motor,

I.INTRODUCTION

The drinkable water is not available for millions of people in various countries due to the non availability of electrical supply in rural areas, where the supply of water is mainly from the rain or from rivers. In such places, the water management through photovoltaic (PV) system is the most efficient and promising way to solve this problem. Even though converters like fuel cell (lead–acid. Lithium-ion batteries) and dc motors available to drive the water pump and giving higher efficiency due to the drawback of the batteries used for pumping system generally have low life span nearly two years which is too low when compared with PV module life of 20 years, requires frequent maintenance and replacement. Whereas PV energy source is cheaper and can reach the poor people need these systems. In this paper three phases IM is replaced by low voltage commercial DC motors due to higher maintenance cost, lower efficiency and also these low voltage DC motors are not frequently available in the market. Therefore this proposed work adopted the three phase induction motor which is available in local markets, and requires less maintenance cost than any other types of motors [14]-[15].

The proposed PV module generates electrical energy directly from solar radiation. This model has to face the challenges like, it has to generate the maximum energy under variable restrictions and also maximize the amount of water pumped [1].



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With these requirements a novel converter is modeled with high efficiency where the availability of energy is low, less cost to use where it is most needed, robustness therefore it needed less possible maintenance and finally has high life time usually 20 years. A new dc/dc boost converter and its control strategy suitable for PV water pumping are proposed [1]-[4] to fulfill the most of the aforementioned features.

II. SOLAR CELL MODEL

Photovoltaic cells are made with two semiconductor materials P-type and N-type. A PV cell is basically a light activated diode that generates a voltage when exposed to sun light. It also be known as a light-emitting diode. Basically PV system is a non-linear device due to its I-V characteristics and is usually described by its equivalent circuit shown in fig.2 and fig.3. The equivalent electrical model of a pv cell contains a current generator, a diode and two resistances. Shunt resistor indicates the leakage currents to the ground; the value of shunt resistor is about 1000 ohms. Series resistor indicates the internal resistance of leads connects to the solar cell and the value of series resistance is 0.001 ohms.

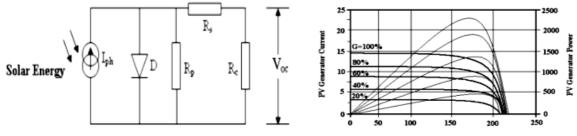


Fig. 2 practicle model of solar cell

Fig. 3 Characteristics of PV Generator

Individual solar cells will offer low voltages (typically 0.5V-0.7V), therefore these cells are usually connected in series or in parallel according to our requirements. The PV array is nothing but the connection of number of solar PV modules. A single unit of PV module can satisfy the requirements of a household. Equations :

$$I = I_{ph} - I_d \left[\exp(q(V + IR_s) / kT_c A - 1) \right] - (V + IR_s) / R_p$$
⁽¹⁾

I ph=Short-circuit current due to sunlight; Id=Current shunted through the diode; R=Load resistance; I=Current through the load; V=Voltage on the load; Rs=series resistance; Rp=Shunt resistance; q=Electron charge; K=Boltzmann's constant;

$$I_{ph} = \left[I_{sc} + \alpha (T_c - T_{ref})\right]G$$
Where
$$\alpha = 0.0012 * I_{sc}$$
(2)

III. PROPOSED CONVERTER

A single PV module is designed in this paper to ensure low cost and for our convenience. This proposed model can drive the motor in the range of 1/3 hp, it is enough to pump the water for single family. The simple operation of this proposed model is, the PV system generates the electrical energy which is in the form of dc output is in the range of 0.5v - 0.6v, thereby to improve the voltage level this low voltage is fed to the DC – DC Boost converter, the output of the boost converter is now fed to the three phase inverter to convert high level DC voltage into AC voltage. In this paper three level diode clamped inverter is presented [5] with the SVPWM controlling technique. By using SVPWM technique the output voltage enhanced when compared to SPWM technique and also it uses all the states in state vector model i.e. SPWM technique couldn't use the states (000 and 111). Because of the two states, this proposed scheme is better than SPWM technique. As the operating characteristic of the PV panel is low, a dc-dc boost converter is needed to have a large voltage conversion ratio. MPPT technique of solar PV system is used to track the maximum amount of power even in cloudy condition.



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Current fed converter is proposed due to the advantages over the voltage fed converter. Voltage fed converters has large input filter capacitor due to high input current ripple and also has large transformer turns ratio to boost the output voltage, therefore voltage-fed converters are not suitable for water pumping applications [19]. Current fed converters generally has an inductor at the input side, thereby having low input current ripple and eliminates the input capacitor. In general current fed converter is derived from the boost converter and having high step-up voltage conversion ratio, this eliminates the need of transformer turns ratio. In this paper, boost converter is proposed due to its small components size, more efficiency and simplicity. Besides, the input current is circulated through the boost inductor ripple free, this feature reduced the oscillations at the operation of PV module, thereby easier to achieve the MPPT.

VI. MPPT CONTROL

The operating point of the PV system is kept at the maximum power point by using MPPT technique. MPPT is one of the hill-climbing algorithms, which is most widely used in PV applications due to its fast dynamic response and simple implementation. Working of MPPT algorithm is mainly based on the nature of the power curve of the PV panel. This power curve can be separated into two sides; one is to the left and other is to the right side of the maximum power point. To get closer to the operating point first analyze the variation of power and voltage then figure out, correct operating region PV panel and adjust the reference voltage. PI controller is used to rise or to condense the motor speed based on this reference voltage.

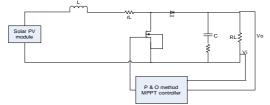


Fig. 4 P&O method MPPT charge controller

The term fill factor (FF) is the combination of open circuit voltage and short circuit current of solar PV panel, the maximum power from the panel determined by fill factor and it can also be defined as the ratio of maximum power obtained from solar panel to the product of open circuit voltage and short circuit current. There are several types of MPPT algorithms namely perturb-and-observe (P&O), incremental conductance and constant voltage etc. The first two methods are often referred to as hill climbing methods,

V. CLOSED LOOP DC-DC BOOST CONVERTER

DC-DC Boost converter will step up the low level DC voltage magnitude (0.5v to 0.7v) into high level DC voltage magnitude. Generally Boost converter operates either in charging mode or in discharging mode, ON and OFF control of the suitable switch decides these modes of operation. Boost converter Circuit consists of an inductor at the source side, a diode and a high frequency switch as main elements in the circuit as shown below.

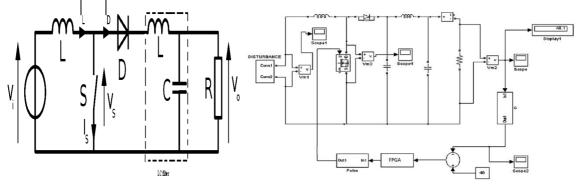


Fig. 5 DC-DC Boost converter

Fig. 6 Simulink model of Closed loop system.



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The relationship between V0 and Vs is given as:

$$V_0 = \frac{V_s}{\left(1 - D\right)} \tag{3}$$

Fig.6. Shows the Closed loop system of DC-DC boost converter, Output voltage is sensed and it is compared with a reference voltage. The error is processed by a FPGA controller; output of FPGA controller adjusts the pulse width to maintain the output voltage constant, input voltage and output voltage of closed loop system. The output voltage reduces and reaches the set value.

VI. LC- FILTER MODELING

In this paper, LC-filter is modeled. It is a second order filter because it has to energy storage elements L and C. when compared to L-filter, it has better filtering ability. The configuration of LC filter is easy to design. The block diagram of an LC-filter is shown below

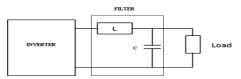


Fig. 7 Basic block diagram of LC-filter

The first step in filter designing is to find the best filter. Calculation of designed impedance is the second step. The final step is to equating the L and C values from the second step using. The impedance form can be calculated by using the below equation.

$$R_d = \frac{V_{Max}}{I_{Max}} \tag{4}$$

The inductance (L) and the capacitance (C) values of the filter can be calculated

$$L = \frac{R_d}{2\Pi f}$$

$$C = \frac{1}{2\Pi f R_d}$$
(5)
(6)

VII. MULTI LEVEL INVERTER

Multilevel inverters are frequently used in Industries for high power applications in the range of MW level. Most of the industries having high power ac drive. These high power ac drives having a difficult when connected to the medium voltage power supplies. This problem has been conquering by multilevel inverters (MLI) with higher voltage levels [15]. Three diode clamped level inverter is proposed in this paper, for water pumping applications and the output of this inverter is fed to the induction motor which drives the water pump effectively [14]. MLI having clamping switches, capacitors, and voltage sources. High power can be handled when compared with conventional two level inverters. By using proper PWM scheme [5]-[8] to the proposed three level diode clamped inverter, harmonics are effectively reduced consequently improves efficiency [15]. The advantage of this three level inverter does not need supplementary coupling transformer exhibits high quality output waveform.

Various topologies have been used for multilevel inverters: (1) neutral-clamped (2) flying capacitors and (3) Cascaded multi cell with separate dc sources. The adopted control strategies for multilevel inverters are: (1) SPWM technique (2)



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selective harmonic elimination method, and (3) Space-vector modulation (SVM). In this paper SVPWM is proposed as a controlling technique.

VIII. SPACE-VECTOR PULSE WIDTH MODULATION (SVPWM)

The output voltage of inverter is increased by using SVWPM technique with PWM signals [5]. In -1980s only this technique was proposed. This technique is the most powerful PWM technique for three-phase inverters. In this paper the output of the inverter is controlled by SVPWM technique used to improve the performance of the pumping system and to minimize the general power losses. Several PWM schemes are implemented with the Microprocessor technology, which helps to reduce the switching losses, harmonics and for precise controlling of inverter output. By using SVPWM technique the output voltage enhanced when compared to SPWM technique and also it uses all the states in state vector model i.e. SPWM technique couldn't use the states (000 and 111) [14]. Because of the two states, this proposed scheme is better than SPWM technique.

The Voltage of DC bus can effectively utilize by SVPWM and generates less THD when compared with the SPWM. Maximum fundamental magnitude obtained by SVPWM controlled inverter is 90.6% and Maximum voltage is 15.5% [14]. Space Vector Representation is shown in fig.7 a reference line or vector is touching all the maximum points of vectors, there by a circle can be formed inside the state map. The region inside the state map and to the circle is called under modulation, the region outside the state model is known over modulation. These regions are depends on modulation index.

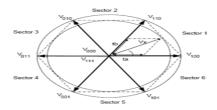


Fig. 8 Space Vector Representation

IX. FPGA BASED DESIGN

Field Programmable Gate Array (FPGA) gives the easy way of implementing the PWM Generation for various Power electronic Applications. Generally FPGA has an interconnection between various logic gates. If any need arise after design on FPGA, it can be easily modified by changing the interconnection of logic blocks. This Reprogramming feature makes it suitable to design using FPGA [16]. Implementation using FPGA takes short time. Therefore this is the best way of implementing digital PWM Generators [8]. Implementation of FPGA-based digital controllers suitable for small designs hence less costly, hence in this thesis FPGA based PWM Generator technique is discussed.[5]-[8].

X. PWM CONTROL OF INVERTER

The PWM control of a DC-AC converter is shown in Fig. 8. The PWM generator generates PWM control signal (VPWM), ANDED with rectangular pulse which is from pulse generator and is fed to switches S1 and S3 [5]-[8]. The inverted rectangular waveform is ANDED with PWM signal and is fed to switches S2 and S4. Thus input DC voltage is modulated into required AC voltage by providing proper turn ON and OFF time of power switches.

The power switches are generally of MOSFET or IGBT. These power switches make the inverter compatible in size. Inverter size is reduced by increasing the switching frequency of power switches. Therefore by proper selection of frequency of power switches by PWM Generator, we get optimized size of Inverter [5].



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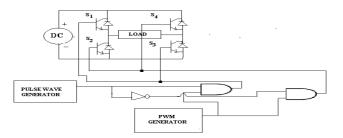
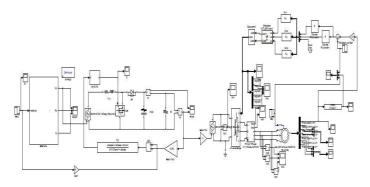


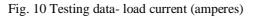
Fig. 9 PWM Control of Inverter

XI. INDUCTION MOTOR

One of the reasons for most widely using of Induction is due to its rugged construction, more reliability, cheap [14]-[15]. These IM motors require less maintenance because IM doesn't have brushes and slip rings. In earlier days DC motors were used in most of the electrical drives due its variable speed characteristics. Now the power electronic technology makes the modern developments in speed controlling methods of the IM, led to their large scale use in almost all electrical drives.

Hence AC motors are tremendously chosen for fixed speed applications in industrial as well as in domestic applications where AC line power is available. Nearly 90% of motors are induction type motors in many applications viz. washers, air conditioners, industrial machinery, dryers, fans, vacuum cleaners, blowers, and other applications. In this paper this IM motor is used for water pumping system as shown in fig.1



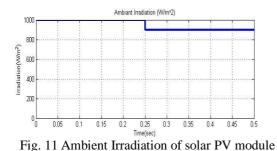


XII. RESULTS AND DISCUSSION

Proposed system fed by Solar, is designed and simulated in MATLAB simulink environment, and the results are presented in this section.

Specifications of PV System:

Temperature variations range of PV System (T) = 298KIrradiance (G) = 1000W/m2-900W/m2.



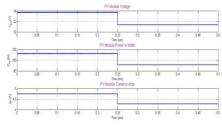


Fig. 12 Solar PV module parameters



(7)

(10)

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DC-DC Boost converter is designed for 24 to 48v with the following specifications. 24 to 60v, 50w Boost Converter Small Signal Transfer Function analysis:

From the specification of DC-DC boost converter, Plant Transfer function (Tp) is given by

$$\frac{-0.1922\,s^2-3.238*10^4\,s+8.572*10^8}{s^2+936.2\,s+1.072*10^7}$$

Plant with Modulator Transfer function (Tmp):

$$\frac{-0.4806 \, \text{s}^2 - 8.094 * 10^4 \, \text{s} + 2.143 * 10}{\text{s}^2 + 936.2 \, \text{s} + 1.072 * 10^7}$$
(8)

Feedback Factor with Tmp Transfer function (Tk):

$$\frac{-0.01001 \, \text{s}^2 - 1686 \text{s} + 4.465 * 10}{\text{s}^2 + 936.2 \, \text{s} + 1.072 * 10^7}$$

(9)

$$\frac{1.236*10^7 s^2 + 1.688*10^{10} s + 5.762*10}{s^3 + 4.625*10^5 s^2 + 5.349*10^{10} s}$$

From the bode plot analysis for above mentioned transfer function of closed loop DC-DC boost converter, it is found that that PM is 44.50 i.e., which meets required stability margin (300-600) of any practical converters. The output voltage and currents of MPPT based DC-Dc converter is fig.13. Additionally, transient parameters of DC-DC converter with MPPT algorithm is calculated with the help of MATLAB Simulink environment and presented in Fig.14. Fig. 15 represents the voltage of solar PV array which is formed by solar PV modules series connection. MPPT based boost converter series group fed to inverter which is operated in closed loop mode with V/F based SVPWM control algorithm and its waveforms are presented in Fig. 16 to Fig. 19.

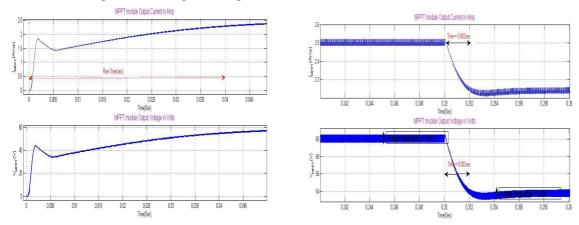


Fig. 13 Module based MPPT DC-DC converter parameters

Fig. 14 DC-DC parameters during transient period



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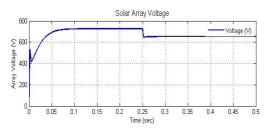


Fig .15 Solar PV Array output voltage

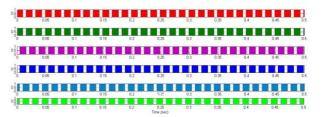


Fig .16 SVPWM based pulse signals

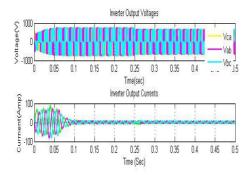


Fig .17 inverter output voltage and current

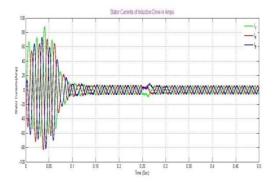


Fig .19 Stator Current.

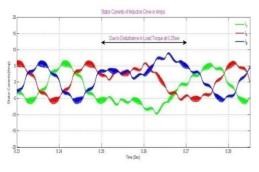


Fig .21 Rotor Currents of induction drive

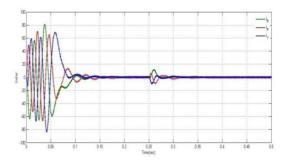


Fig .18 Rotor Current

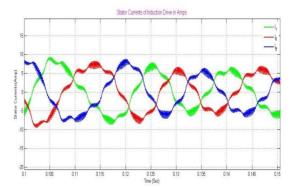


Fig .20 Stator Currents of induction drive

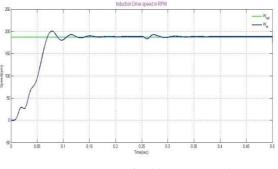


Fig .22 Rotor speed



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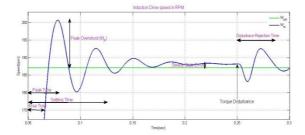


Fig .23 Speed of Induction drive

Fig. 16 depicts that SVPWM based control signals which are controlling the inverter switches in closed loop mode to get required speed, and produce output parameters of inverter are presented in Fig. 17. In Fig. 18, actual speed of induction motor and reference speed is presented. Additionally, dynamic time domain parameters are calculated in MATLAB Simulink environment with sudden change in load at 0.25sec and presented in Fig. 19.

XII. CONCLUSION

Thus it allows each node with message to decide whether to copy the message to a path node by optimizing its transmission effort in order to provide a sufficient level of message delay. Using a channel selection scheme provides spectrum utilization while it minimizes the interference level to primary system. Using trustworthy algorithm, it improves the trustworthiness of the Spectrum sensing in CR-Networks. It enables network nodes to adaptively regulate their communication strategies according to dynamically changing network environment.

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