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Use Of Unitary Differential Space Time Frequency Codes For MB-OFDM UWB Transceiver

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Abstract — In recent day's military and government applications of UWB communications have demanded the accommodation of larger numbers of users, each with ever increasing bandwidth requirements. Spectral resources are potentially wasted in order to avoid narrowband interference in the case single carrier based multi band system. For channel state information channel estimation requires large number of pilot symbols need to be transmitted, thus reducing the bandwidth efficiency and it will also lead complexity in the receiver side. This paper proposes unitary differential space-time-frequency codes (DSTFCs) for MB-OFDM UWB communications, which increase the system bandwidth efficiency because no channel state information (CSI) is required. The proposed system would be useful when CSI is unavailable at the receiver. The paper also quantifies for the variable puncture rate Forward error correction scheme for significant improvement in the bit error performance of proposed MB-OFDM. Performance of proposed system is analyzed in deep fading environment (Nagakami-m channel) results are presented which show that the variable rate forward error correction (FEC) with MB-OFDM UWB communication is better than that with conventional system, and the BER performance is also be improved significantly.

Index Terms — reconfigurable FFT architecture, radix- 2^k algorithm, orthogonal frequencydivision multiplexing(OFDM), ultra wideband (UWB).

I. INTRODUCTION

In telecommunications, 4G is the fourth generation of cellular wireless standards. It is a successor to 3G and 2G

families of standards. Multi-band orthogonal frequencydivision multiplexing (MB-OFDM) is one of 4G ultra wideband (UWB) radio standards, which provides highspeed connectivity in a wireless personal area network (PAN) [1] with specification of the data rates from 53.3 to 480 Mbps [2]. Due to the high data rates, the MB-OFDM standard requires to process large amount of computations in very short time; its modem has to compute one symbol that consists of 165 complex numbers in every 312.5 ns. Even though its performance requirement results in large hardware complexity, a low power design with small chip size is absolutely essential for applying this technology to portable handheld devices. Also, an operating frequency of a circuit is one of the dominant factors that determine power consumption.

MB-OFDM defines two constellation mapping schemes: QPSK and DCM modulations. The QPSK spreads data into several subcarriers and the DCM requires data reordering. The spreading and reordering processes involve non-trivial amount of buffer storages and also latency.

Conventionally those processes are done as separate phases: interleaving first and then spreading or reordering. But, we can unify the spreading and the (inverse)reordering with the (de)interleaving process. With the proposed interleaver architecture, we can perform the spreading before the interleaving process by fully utilizing array cells of our interleaver.

II . MULTIBAND SYSTEM BASED ALGORITHM

The N-point DFT is formulated as



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$$X(k) = \sum_{n=0}^{N-1} x(n) W_N^{nk}, k = 0, 1, \dots N - 1$$
(1)

Where the twiddle factors is defined as $W_{N}^{nk} = e^{\frac{2\pi nk}{N}}$ The n denotes the time index and the k

 $W_N = e^{-1}$. The n denotes the time index and the k denotes the frequency index. The radix 2^k algorithm can be derived by integrating twiddle factor decomposition through a divide and conquer approach.

A. UWB specifications.

This standard specifies an ultra-wideband (UWB) physical layer (PHY) for a wireless personal area network (PAN), utilizing the unlicensed 3100 - 10600 MHz frequency band, supporting data rates of 53.3, 80, 106.7, 160, 200, 320, 400, 480, 640, 800, 960 and 1024 Mb/s. Support for transmitting and receiving data rates of 53.3, 106.7, and 200 Mb/s using the convolutional code shall be mandatory. For each of the rates 160, 200, 320, 400 and 480, if LDPC coding is provided then convolution coding should also be provided.

The UWB spectrum is divided into 14 bands, each with a bandwidth of 528 MHz the first 12 bands are then grouped into 4 band groups consisting of 3 bands. The last two bands are grouped into a fifth band group. A sixth band group is also defined within the spectrum of the first four, consistent with usage within worldwide spectrum regulations. At least one of the band groups (BG1 - BG6) shall be implemented.

B. Forward Error Correction

The puncturer omits some of coded bits in order to support different code rates with one convolutional encoder. The depuncturer inserts dummy bits for the omitted bits.

Our system used a convolutional encoder with code rate 1/3 and constraint length 7. To decode the convolutional codes at 8-bits per cycle, we implemented a four-stage radix-4 Viterbi decoder by extending the twostage radix-4 decoder proposed in [10], where the traceback length is 48. The two-stage radix-4 Viterbi decoder was implemented by cascading two radix-4 decoders.

III . PROPOSED SCHEME

In this brief, Space-time frequency encoder which encodes a single stream through space using all the transmit antennas and through frequency by sending each symbol at different frequencies.

A. Functional Units

In a MB-OFDM system, a guard interval (9.5 nanoseconds) is appended to each OFDM symbol and a zero-padded prefix (60.6 nanoseconds) is inserted at the beginning of each OFDM symbol. The guard interval ensures that there is sufficient time for the transmitter and receiver to switch to the next carrier frequency. A zero-padded prefix provides both robustness against multi-path and eliminates the need for power back-off at the transmitter. More details about the zero-padded prefix will be described in a later section.

The structure of the MB-OFDM solution is very similar to that of a conventional wireless OFDM physical layer, except that the carrier frequency is changed based on the time-frequency code. In addition, other modifications have been made to reduce the area and size.

MULTI-BAND orthogonal frequency-division multiplexing (MB-OFDM) is one of ultra-wideband (UWB) radio standards, which provides high-speed connectivity in a wireless personal area network (PAN) [1] with specification of the data rates from 53.3 to 480 Mbps [2]. Due to the high data rates, the MB-OFDM standard requires to process large amount of computations in very short time; its modem has to compute one symbol that consists of 165 complex numbers in every 312.5 ns. Even though its performance requirement results in large hardware complexity, a low power design with small chip size is absolutely essential for applying this technology to portable handheld devices.

B. MB-OFDM BASEBAND MODEM

Fig.1 and 2 shows an overall architecture of our MB-OFDM PHY baseband modem that supports both transmission (TX) and reception (RX) according the MB-OFDM protocol in the standard [2]. Our architecture was designed to process eight complex numbers of TX and RX



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signals at one time with 8-way parallel data paths. By having the high degree of parallel data paths, the baseband modem can operate at 66MHzthat is one-eighth of the sampling frequency 528 MHz with providing up to 480 Mbps throughput.

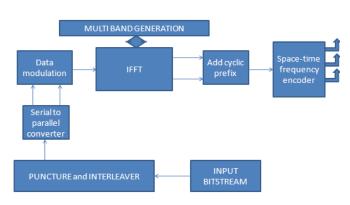


Fig .1 MB OFDM Transmitter

The baseband modem is composed of various components which process the incoming data and then deliver the processing results to each following component in a streaming fashion. We used an 8-bit fixed-point representation for complex numbers that are passed through input and output ports of each component.

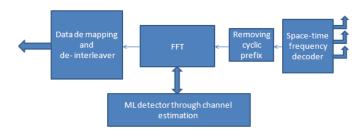


Fig. 2 MB OFDM receiver

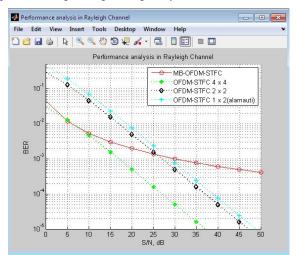


Fig .3 MB OFDM vs. STFC

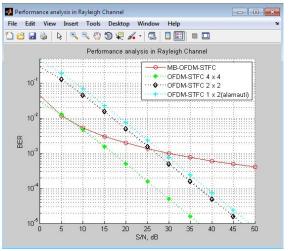


Fig .4 performance of MB OFDM in UWB chanenel

IV.CONCLUSION

MB-OFDM is used to enhance multiple user atbthe same time and by using STFC overall throughput is increased and bit error performance is analysed and evaluated factors such as speed, area,gate count which is analysed using MATLAB is implemented by using MODELSIM and QUARTZ –II.



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REFERENCES

[1] Taesang Cho and Hanho Lee, "A high speed low- complexity modified radix -25 FFT processor for high rate WPAN applications," IEEE Trans. Very Large

Scale Integr.(VLSI) Syst., vol. 21, no. 1, pp. 187-191, January.2013.

[2]Y. Lin, H. Liu, and C. Lee, "A 1-GS/s FFT/IFFT processor for UWB applications," IEEE J. Solid-State Circuits, vol. 40, no. 8, pp. 1726-1735, Aug. 2005.

[3] J. Lee and H. Lee, "A high-speed two-parallel radix -24 FFT/IFFT processor for MB-OFDM UWB systems," IEICE Trans. Fundam., vol. E91-A, no. 4, pp. 1206-1211, Apr. 2008.

[4] Y. Chen, Y. Tsao, Y. Wei, C. Lin, and C. Lee, "An indexed-scaling pipelined FFT processor for OFDM-based WPAN applications," IEEE Trans. Circuits Syst. II, Exp. Briefs, vol. 55, no. 2, pp. 146–150, Feb. 2008.

[5] M. Shin and H. Lee, "A high-speed four-parallel radix -2⁴ FFT processor for UWB applications," in Proc. IEEE Int. Symp. Circuits Syst. (ISCAS), 2008, pp. 960–963.

[6] S. Tang, J. Tsai, and T. Chang, "A 2.4-GS/s FFT processor for OFDM based WPAN applications," IEEE Trans. Circuits Syst. II, Exp. Briefs, vol. 57, no. 6, pp. 451-455, Jun. 2010.

[7] S. Huang and S. Chen, "A green FFT processor with 2.5-GS/s for IEEE

802.15.3c (WPANs)," in Proc. Int. Conf. Green Circuits Syst. (ICGCS), 2010, pp. 9–13.

[8] T. Cho, H. Lee, J. Park, and C. Park, "A high-speed low-complexity modified radix -2⁵ FFT processor for gigabit WPAN applications," in Proc.

IEEE Int. Symp. Circuits Syst. (ISCAS), 2011, pp. 1259–1262.
[9] A. Cortes, I.Velez, and J. F. Sevillano, "Radix r^k FFTs: Matrical representation and SDC/SDF pipeline implementation," IEEE Trans. Signal Process., vol. 57, no. 7, pp. 2824–2839, Jul. 2009.

[10] K. Cho, K. Lee, J. Chung, and K. Parhi, "Design of low-error fixedwidth modified booth multiplier," IEEE Trans. Very Large Scale Integr. (VLSI) Syst., vol. 12, no. 5, pp. 522-531, May 2004.

[11] R. I. Hartley, "Subexpression sharing in filters using canonic signed digit multipliers," IEEE Trans. Circuits Syst. II, Exp. Briefs, vol. 43,no. 10, pp. 677-688, Oct. 1996.