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# Multilevel Inverter for Grid-Connected PV SystemEmploying MPPT and PI Controller

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**Abstract**: This paper presents a single phase five level inverter topology for grid connected PV system. A new pulse width modulated (PWM) control scheme was introduced. In this scheme two reference signals identical to each other and a triangular carrier wave was used to generate the switching signals. MPPT algorithm was implemented to catch the maximum power point in the PV array. A proportional-integral control algorithm was also implemented for obtaining high dynamic performance.

Keywords:grid connected, photovoltaic (PV), pulse width modulation (PWM), proportional-integral controller, (MPPT)

### I. INTRODUCTION

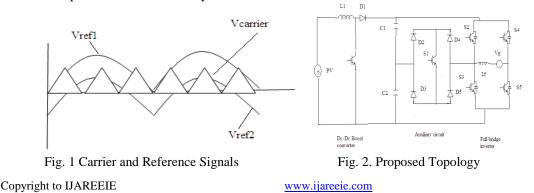
The demand for renewable energy has increased significantly over the years because of shortage of fossil fuels. Among various energy sources, solar energy and wind energy have become very popular. Solar electric energy has grown significantly by 20-25% per annum over the past 20 years. PV inverter which is the heart of the PV system is used to convert dc power obtained from PV module into ac power.

A typical single phase three level inverter adopts full bridge configuration by using approximate sinusoidal modulation technique as the power circuits. The output voltage then has the following three values: zero, positive(+Vdc) and negative(-Vdc)[7]. The harmonic component of the output voltage are determined by the carrier frequency and switching functions. Therefore harmonic reduction is limited to a certain degree.

To overcome this limitation, this paper presents a five level PWM inverter whose output voltage can be represented in the following levels: zero, +1/2 Vdc, +Vdc, -1/2 Vdcand -Vdc[2]. As the number of output voltage levels increases, the harmonic content can be reduced. This inverter topology uses two reference signals instead of one reference signal, to generate signals for the switches. Both the reference signals are identical to each other, except for an offset value equivalent to the amplitude of the carrier signal as shown in Fig. 1.

One of the problems in the PV generation system is the amount of the electric power generated by solar arrays always changing with weather conditions, i.e. the intensity of the solar radiation. A maximum power point tracking (MPPT) method which is able to make good use of the electric power generated in any weather conditions.

Because the inverter is used in a PV system, a proportional – integral (PI) current control scheme is employed to keep the output current sinusoidal and to have high dynamic performance under rapidly changing conditions and to maintain the power factor at near unity.





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#### II. FIVE-LEVEL INVERTER TOPOLOGY

The proposed single phase five level inverter topology is shown in Fig. 2. The inverter adopts full bridge configuration with an auxiliary circuit. PV arrays are connected to the inverter through a dc –dc boost converter. The dc –dc boost converter is used to step up inverter output voltage  $V_{inv}$  to be more than  $\sqrt{2}$  of grid voltage to ensure power flow from the PV arrays into the grid. A filtering inductance  $L_f$  is used to filter the current injected into the grid. The injected current must be sinusoidal with low harmonic distortion. The switching instant is determined from the crossing of the carrier and the modulating signal.

#### III. OPERATING PRINCIPLE OF THE PROPOSED INVERTER

Because PV arrays are used as input voltage sources, the voltage produced by the array in known as  $V_{arrays}$ .  $V_{arrays}$  is boosted by a dc-dc boost converter to exceed  $\sqrt{2}V_g$ . The proposed inverter is to generate five level output voltages, i.e. zero, +1/2 Vdc, +Vdc, -1/2 Vdc and -Vdc as in Fig. 3. Auxiliary circuit which consist of four diodes and a switch s<sub>1</sub> is used between the dc bus capacitor and the full bridge inverter. Proper switching control of auxiliary circuit can generate half level of PV supply voltage, i.e. +1/2 Vdc and -1/2 Vdc[4].

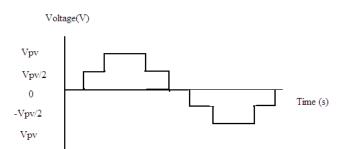


Fig. 3. Ideal Five Level Inverter Output Voltage

Two reference signals  $V_{ref1}$  and  $V_{ref2}$  will take turns to be compared with the carrier signal at a time. If  $V_{ref1}$  exceeds the

peak amplitude of the carrier signal  $V_{carrier}$ ,  $V_{ref2}$  will be compared with the carrier signal until it reaches zero. At this point onwards,  $V_{ref1}$  takes over the comparison process until it exceeds  $V_{carrier}$ . This will lead to switching pattern as shown in Table 1.

INVERTER OUTPUT VILTAGE DURING S1-S5 SWITCH ON ABD OFF					
S1	S2	<b>S3</b>	S4	S5	Vout
Off	On	Off	Off	On	+Vpv
On	Off	Off	Off	On	+Vpv/2
Off	Off	Off	Off	On	0
Off	Off	On	Off	Off	0
On	Off	Off	On	Off	-Vpv/2
Off	Off	On	On	Off	-Vpv

TABLE I

#### **IVMPPT TECHNIQUE**

Tracking the maximum power point(MPP) of the photovoltaic array is usually an essential part of a PV system. As such many MPP tracking (MPPT) methods have been developed and implemented. The methods vary in complexity, sensors required, convergence speed, cost etc.. Here I used incremental conductance method[11]. The incremental conductance method is based on the fact that the slope of the PV array power curve is zero at the MPP, and negative on the right, as given by



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 $\frac{dp}{dv} = 0 , \quad \text{at MPP} \\ \frac{dp}{dv} < 0 , \quad \text{left of MPP} \\ \frac{dp}{dv} > 0 , \quad \text{right of MP} \end{cases}$ (1)

(1) can also be written as

$$\frac{\Delta I}{\Delta V} = -\frac{I}{V} \quad , \qquad \text{at MPP}$$

 $\frac{\Delta I}{\Delta V} > -\frac{I}{V} , \qquad \text{left of MPP}$  $\frac{\Delta I}{\Delta V} < -\frac{I}{V} , \qquad \text{right of MPP}$ 

The MPP can thus be tracked by comparing the instantaneous conductance (I/V)to the incremental conductance  $(\Delta I/\Delta V)$  as shown in the flow chart Fig. 4. V<sub>ref</sub> is the reference voltage at which the PV array is forced to operate. At the MPP, V<sub>ref</sub> equals to V<sub>MPP</sub>. Once the MPP is reached, the operation of the PV array is maintained at this point unless a change in  $\Delta I$  is noted, indicating a change in atmospheric conditions and the MPP. The algorithm decrements or increments the V<sub>ref</sub> to track the new MPP.

### V. FIVE LEVEL INVERTER WITH CONTROLL ALGOTITHM

The feedback controller used in this applications utilizes the PI algorithm as shown in Fig. 5 The current injected into the grid, also known as grid current  $I_g$ , is sensed and feedback to a comparator which compares it with the reference current  $I_{ref}$ .  $I_{ref}$  is obtained by sensing the grid voltage and converting it to reference current and multiplying it with constant m. Constant m is derived from the MPPT algorithm.



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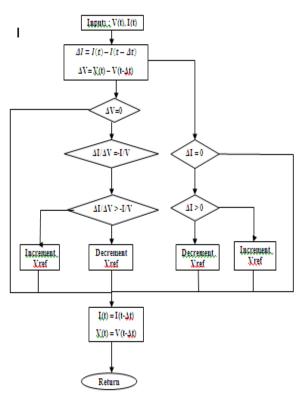


Fig. 4 Incremental conductance algorithm

The incremental conductance algorithm is used to extract maximum power from PV array and deliver it into the inverter. The instantaneous current error is fed to a PI controller. The integral term in the PI controller improves the tracking by reducing the instantaneous error between the reference and the actual current. The resulting error signal u which forms  $V_{ref1}$  and  $V_{ref2}$  is compared with a triangular carrier signal, and intersections are sought to produce PWM signals for the inverter switches.

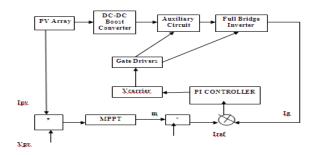


Fig. 5.Five Level Inverter With control Algorithm

### VI SIMULATION AND EXPERIMENTAL RESULTS

#### A. Modelling of PV

A solar cell is the building block of a solar panel. The equivalent circuit models of PV cell is given in fig 6. The terminal equation for the current and voltage of the array becomes as follows.



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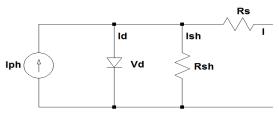


Fig. 6. Equivalent circuit model of PV

$$Ipv = NpIph - NpIs \left[ exp\left(\frac{\frac{q\left(\frac{v}{Ns} + IRs\right)}{Np}}{KTcA}\right) - 1 \right] - \left(\frac{\frac{Npv}{Ns} + IRs}{Rsh}\right)$$
  
Where

where

$$Iph = [Isc + K(Tc - Tref)] * \lambda$$

$$Is = Irs(\frac{Tc^{3}}{Tref}) \exp[\frac{1}{qEg}(\frac{1}{Tref} - 1/Tc)/KA]$$
$$rs = Isc/\left[\exp\left(\frac{qVoc}{NsKTcA}\right) - 1\right]$$

By using above equations a PV cell was simulated in matlab. Simulation results were given in fig 7. For the output characteristics the output voltage of PV is 21V and power is 37MW.

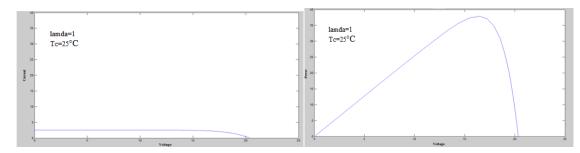


Fig 7. I-V & P-V Characteristics of PV Module

#### B. Simulation Results of Inverter

In order to verify that the proposed inverter can be practically implemented in a PV system, simulations were performed by using MATLAB SIMULINK. Fig. 9 shows the switching status for upper and lower switches . Note that one leg of the inverter is operating at a high switching rate equivalent to the frequency of the carrier signal, whereas the other leg is operating at the rate of fundamental frequency. The output voltage and current waveforms are shown in Fig. 9. Note that we are obtained an output voltage of 400V.



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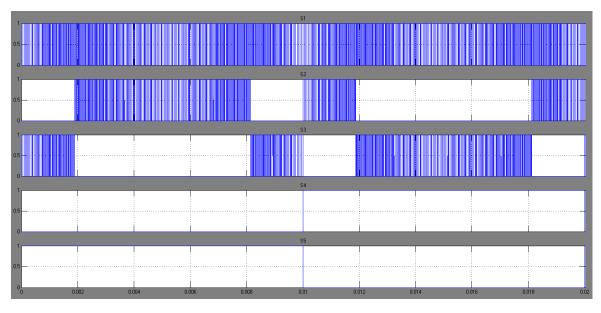


Fig. 8. Switching pattern

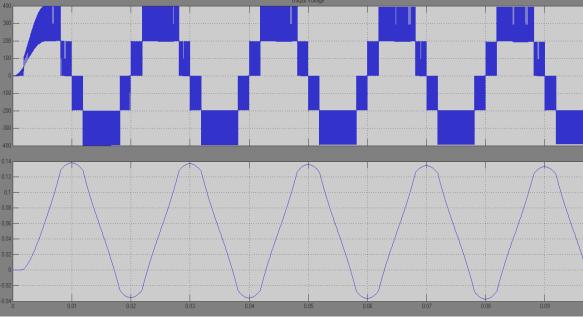


Fig 9. Output Voltage and Current Waveforms of five level inverter

In order to verify the improvement of the output voltage I also simulated a three level inverter. The output voltage and current wave forms are given in Fig 10. The SIMULINK MODEL of three level inverter can be obtained from five level inverter by replacing the auxiliary circuit and by modifying the switching signals.



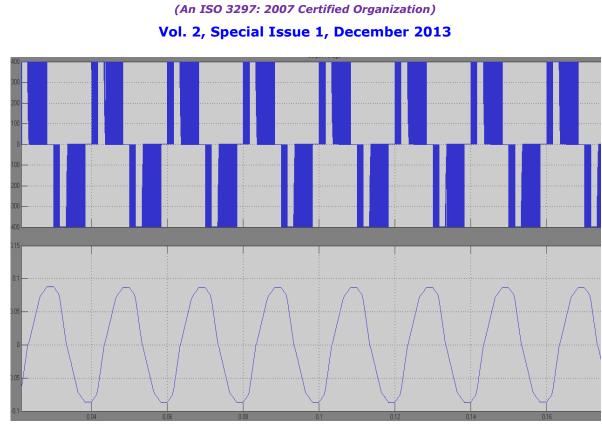


Fig 10. Output Voltage and Current Waveforms of a Three level Inverter

THD of the proposed inverter and three level inverter were measured and verified. THD of three level inverter and proposed inverter were given in Fig 11. By the analysis it is clear that THD can be reduced to a comparatively low value in the proposed inverter. Thus we can conclude that by increasing the level of the output voltage we can reduce harmonic content thereby improving the performance of a PV array.

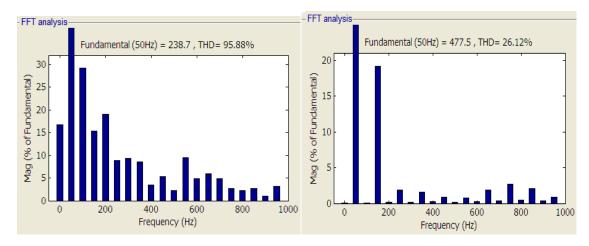


Fig. 11. THD Analysis

### C. Experimental Results

Proposed inverter was implemented with hard ware. Fig. 12 shows the experimental setup. Output voltage and THD was measured using fluke meter and were shown in Fig. 13.



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Fig. 12 Experimental Setup

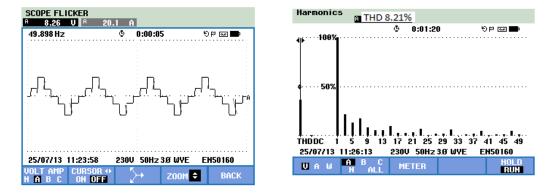


Fig. 13 Output Voltage & THD

### VII CONCLUSION

This paper presented a single phase five level for PV application. I simulated PV modele, then the proposed inverter was implemented with the pv model. The circuit topology and operation principle of the proposed inverter were analysed in derail. MPPT control algorithm with a PI controller was implemented in the control loop. Proposed inverter was compared with a three level inverter. Simulation results shows that THD level can be reduced to a low value. Proposed inverter was obtained in a fluke meter.

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