



Realization of AC-AC Converter Using Matrix Converter

B.Vamsi Krishna

Associate Professor, Bharath University, Chennai-600073, India

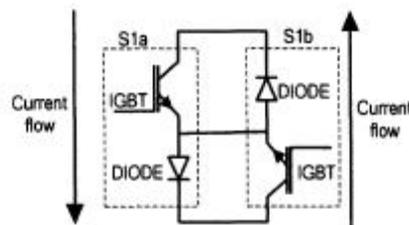
ABSTRACT-The aim of this paper is to describe work involved in investigations on the implementation of single phase matrix converter (SPMC) as an AC-AC converter subjected to passive load conditions. The successful implementation of the SPMC had been conceived, designed and realized by synthesizing the output using the well - known sinusoidal pulse width modulation (SPWM) technique with IGBTs as power switching devices. To show the validity of results, Simulations were done by means of the power system block within the MATLAB/ SIMULINK software package.

I.INTRODUCTION

Matrix converter has been known to offer an "all silicon" solution for AC-AC Conversion, removing the need for reactive energy storage components used in conventional rectifier-inverter based system .Its topology was first proposed by Gyugyi in 1976. The circuit is arranged such that any output lines of the converter can be connected to any input lines. The main purpose of this paper is to describe the successful implementation of the SPMC that had been conceived, designed and realised by synthesizing the output using the well-known SPWM technique.

II.THE SPMC

The SPMC requires 4 bi-directional switches each capable of conducting current in both directions, blocking forward and reverse voltages. The IGBTs were used as power switches which combines the positive attributes of BJTs and MOSFETs. BJTs have lower conduction losses in the on-state, especially in devices with larger blocking voltages, but have longer switching times, especially at turn-off while MOSFETs can be turned on and off much faster, but their on-state conduction losses are larger, especially in devices rated for higher blocking voltages. Hence, IGBTs have lower on-state voltage drop with high blocking voltage capabilities in addition to fast switching speeds. It is also popular amongst researchers that could lead to high-power applications with reasonably fast switching frequency for fine control.



1. Bidirectional Switch Module

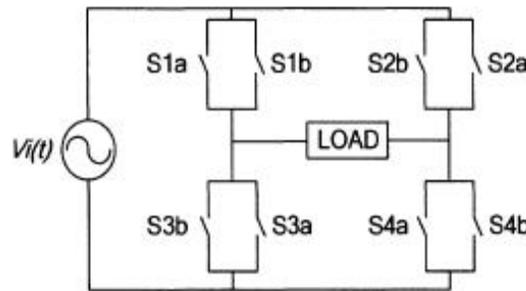
The matrix converter is a forced commutated converter which uses an array of controlled bidirectional switches as the main power elements to create a variable output voltage system With unrestricted frequency. It does not have any dc-

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link circuit and does not need any large energy storage elements. The matrix converter has several advantages over traditional rectifier-inverter type power frequency converters. It provides sinusoidal input and output waveforms, with minimal higher order harmonics and no subharmonics; it has inherent bi-directional energy flow capability; the input power factor can be fully controlled. Last but not least, it has minimal energy storage requirements, which allows to get rid of bulky and lifetime-limited energy-storing capacitors. The key features of matrix converter are Simple and compact power circuit, Generation of load voltage with arbitrary amplitude and frequency, Sinusoidal input and output currents, Operation with unity power factor for any load, Regeneration capability.



2.SPMC Circuit Configuration

The input and output voltage of SPMC is given by (1) and (2) respectively with loads represented in (3),

$$v_i(t) = \sqrt{2}V_i \sin \omega t \quad (1)$$

$$v_o(t) = \sqrt{2}V_o \sin \omega t \quad (2)$$

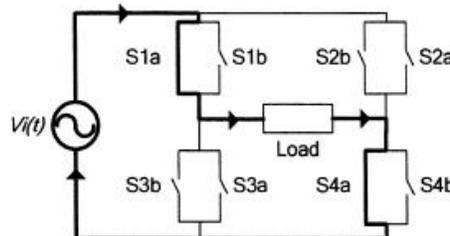
$$v_o(t) = Ri_o(t) + \frac{Li_o(t)}{dt} \quad (3)$$

2.1 SWITCHING STRATEGIES:

Driver circuits are designed to generate the SPWM patterns that are used to control the power switches, comprising IGBTs in the SPMC circuit. The switching angles, of the 4 bidirectional switches s_{ij} ($i = 1,2,3,4$ and $j = a, b$) where 'a' and 'b' are representing driver one and two respectively.

The following rules are then applied;

2.1.1 At any time't', only two switches s_{ij} ($i = 1, 4$ and $j = a$) will be in 'ON' state and conduct the current flow during positive cycle of input source (state 1)



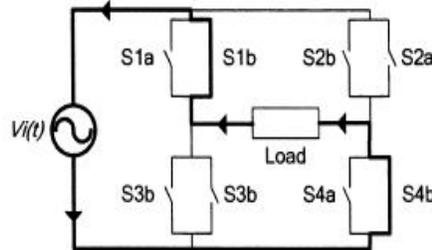
3.State1 (Positive Cycle)

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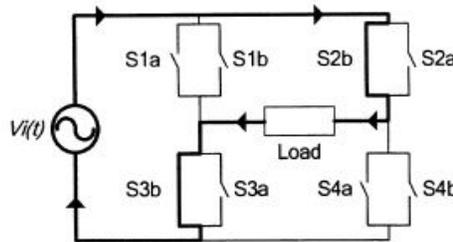
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2.1.2 At any time 't' only two switches s_{ij} ($i = 1, 4$ and $j = b$) will be in 'ON' state and conduct the current flow during negative cycle of input source (state 2).



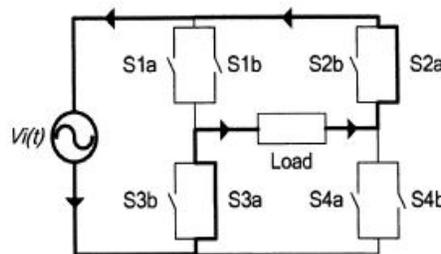
4.State2 (Negative Cycle)

2.1.3 At any time 't' only two switches S_{ij} ($i = 2, 3$ and $j = b$) will be in 'ON' state and conduct the current flow during positive cycle of input source (state 3).



5 .State3 (Positive Cycle)

2.1.4 At any time 't' only two switches s_{ij} ($i = 2, 3$ and $j = a$) will be in 'ON' state and conduct the current flow during negative cycle of input source (state 4)



6 .State4 (Negative Cycle)

2.2 SEQUENCE OF SWITCHING CONTROL:

The output frequency is synthesized in multiples of input frequency of 50Hz (say 50Hz, 100Hz and 150Hz). The sequences of switching are dependent on the time interval and state of the driver circuit.

Consider the output frequency is 50 Hz. To achieve this, when the supply voltage is positive the switch is in state 1 (S1a and S4a are turned ON). On the other hand switching state 2 are used during negative cycle to produce the next half Cycle



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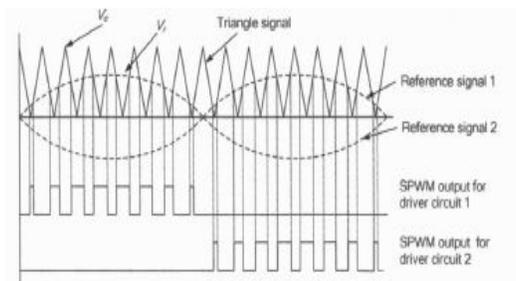
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Input Frequency	Output Frequency	Time Interval	State	Switch "ON"
50 Hz	50 Hz	1	1	S1a and S4a
		2	2	S1b and S4b
	100 Hz	1	1	S1a and S4a
		2	3	S2b and S3b
		3	4	S2a and S3a
	150 Hz	4	2	S1b and S4b
		1	1	S1a and S4a
		2	3	S2b and S3b
		3	1	S1a and S4a
		4	2	S1b and S4b
		5	4	S2a and S3a
		6	2	S1b and S4b

7.Sequence Of Switching control

III.SINUSOIDAL PULSE WIDTH MODULATION:

The sinusoidal pulse width modulation is commonly used in industrial applications. The Sinusoidal Pulse Width Modulation (SPWM) is a well known wave shaping technique in power electronics. For realisation, a high frequency triangular carrier signal, V_c , is compared with a sinusoidal reference signal, V_r of the desired frequency. The crossover points are used to determine the switching instants. The magnitude ratio of the reference signal (V_r) to that of the triangular signal (V_c) is known as the modulation index (m_i). The magnitude of fundamental component of output voltage is proportional to m_i . The amplitude V_c of the triangular signal is generally kept constant. By varying the modulation index, the output voltage could be controlled. The frequency of reference signal f_r determines the inverter output frequency f_o , and its peak amplitude A_r controls the modulation index M , and then in turn the RMS output voltage V_o . The number of pulses per half cycle depends on the carrier frequency.



8.Formation Of SPWM

IV.MODELLING OF SPMC

Main model of SPMC consists of bidirectional switch module and driver circuits. The multiplier unit in the driver circuit is used to vary the magnitude of the sampled sinusoidal reference signal in order to change the modulation index of the SPWM. The modulation index input data is in the form of an integer ranging from 1 to 10. The output from the multiplier unit will then be used to compare with the carrier signal, thus producing the required SPWM switching signal.

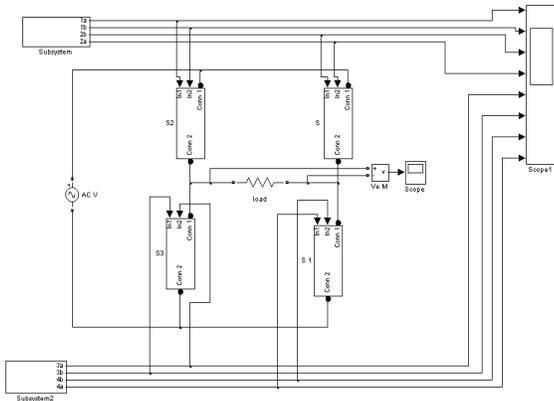
The MATLAB model of the SPMC was developed by using the sim power system block set. Bidirectional switch module is developed by interconnecting two IGBT's. Computer simulation of SPMC is done using MATLAB SIMULINK

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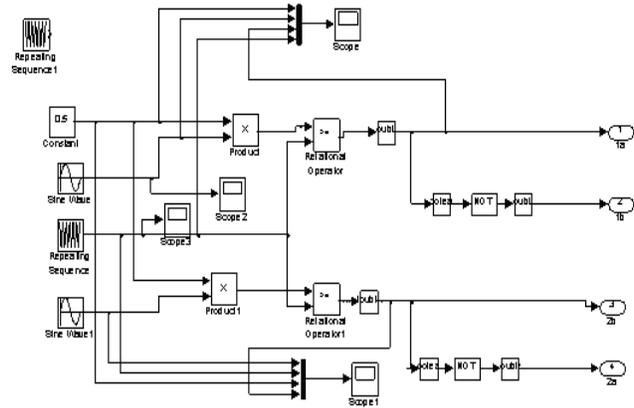
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and the corresponding results for R and RL load are obtained. Simulation parameters are explained in the table. Figure shows the developed model for SPMC by using MLS that performs the operation of a direct AC-AC converter.

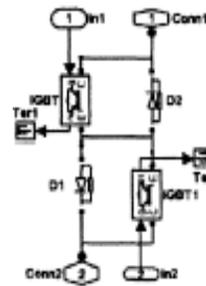


9. Simulation model of SPMC



1

10. Simulation Model of Driver Circuit



11. Simulation Model of Bi-Directional Switch

V.SIMULATION RESULTS

The SPMC was simulated with switching strategy being implemented on all the four states and the results of SPMC model are shown in the below figures for 50Hz, 100Hz, 150Hz, that were synthesized from a supply operating at fundamental frequency of 50Hz. The simulation model could be used to study the behavior of the SPMC as an AC-AC converter under a variety of operating conditions, including different reference frequency.

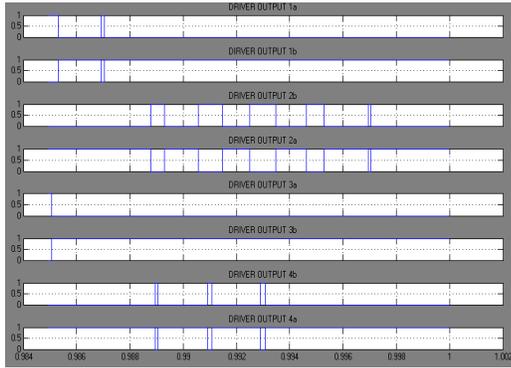
Carrier Frequency	1 KHz
Reference Frequency	50Hz
Input Source	50V

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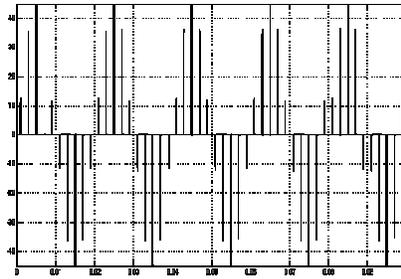
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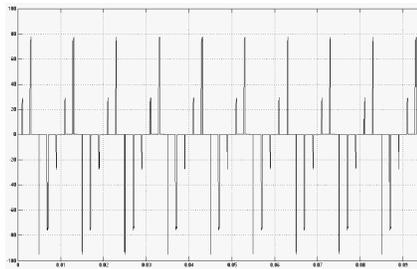
12.Circuit Parameters



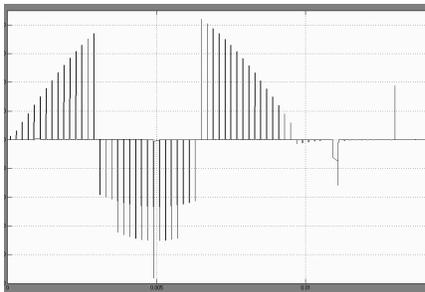
13.Output from Driver Circuit



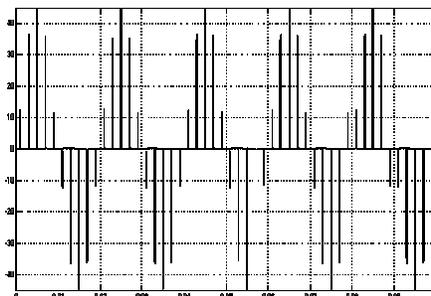
14.Simulation Result of SPMC at $f_c=50\text{Hz}$ for Resistive load



15.Simulation Result of SPMC at $f_c=100\text{Hz}$ for Resistive load



16.Simulation Result of SPMC at $f_c=150\text{Hz}$ with Resistive load



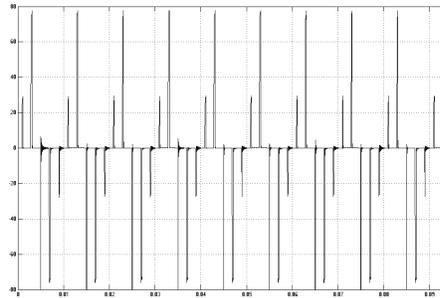


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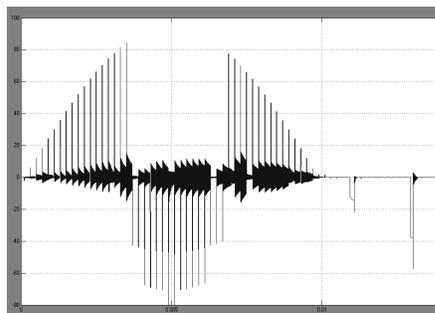
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17. Simulation Result of SPMC at $f_c=50$ Hz with
RL load



18. Simulation Result of SPMC at $f_c=100$ Hz with
RL load



19. Simulation Result of SPMC at $f_c=150$ Hz with
RL load

VI. CONCLUSION

In this paper, a single-phase to single phase matrix converter with passive load condition and its advantages against the commercial devices were presented. The SPMC circuit is designed and simulated which does not require the dc link as in the conventional model. The simulated output waveforms show the successful implementation of the SPMC circuit in the MLS Software. These simulated results of SPMC illustrates that it is feasible to realize the matrix converter as a frequency step-up converter. Further works are required to implement low-pass filter at the output to ensure continuous waveforms that are desirable in actual applications. Also when using with RL load, Undesirable spikes seem to appear with a reasonable degree of magnitude that requires elimination. This probably could be solved using novel commutation strategies.

REFERENCES

- [1] Abdollah Koei & Subbaraya Yuvarajan, "Single-Phase AC-AC converter Using Power Mosfet's," IEEE Transaction on industrial Electronics, Vol. 35, No.3, August 1988, pp.442-443.



International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering

(An ISO 3297: 2007 Certified Organization)

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- [2] Biaajjerg F, Casadei D, Klumpner C, and Matteini, "Comparison of two current modulation strategies for matrix converters under unbalanced input voltage conditions," IEEE Transactions on Industrial Electronics, Vol.49, April 2002, pp 289 – 296.
- [3] Cho, J.G., and Cho, G.H., " Soft-switched Matrix Converters for High Frequency direct AC to AC Power Conversions," Int. J.Electron., 1992, 72,(4), pp. 669 – 680.
- [4] Firdaus,S., Hamzah, M.K., "Modelling and Simulation of a single-phase AC-AC matrix converter usig SPWM", Proceedings on Student Conference on Research and Development, 16-17 July 2002, pp. 286 – 289.
- [5] Gyugi,L and Pelly, B.R, "Static Power Chargers, Theory, Performance and Application," John Wiley & Son Inc, 1976.
- [6] Hosseini, S.H.; Babaei, E, "A new generalized direct matrix converter", IEEE International Symposium of Industrial Electronics, 2001. Proc.ISIE 2001 . Vol(2), pp. 1071 – 1076.
- [7] Klumpnetr C, Boldea I, Nieisen P, and Blaabjerg F, "A new matrix converter – motor (MCM) for industry applications," in Conf. rec. IEEE – IAS Annu, Meeting, 2000, CD – ROM.
- [8] Klumpner C, Boldea I, Nieisen P, and Blaabjerg F, " New steps toward a low cost power electronic building block for matrix converters," in Conf. Rec. IEEE – IAS Annu. Meeting, 1999, CD – ROM.
- [9] M.H.Rashid, "Power Electronics Handbook", Academic Press 2001
- [10] Oyama,J.,Higuchi,T.,Yamada,E., Koga,T., and Lipo,T., " New Control Strategies for Matrix converter," IEEE Power Electron. Spec. Conf. Rec., 1989, pp. 360 – 367.
- [11] Sobezyk,T., "Numerical Study of Control Strategies for Frequency Converter with a Matrix Converter," Proceedings of Conference on Power Electronics and Motion Control , Warsaw, Poland, 1994,pp.497 – 502.
- [12] Wheeler, P.W.,Rodriguez, Clare, J.C., Empringhan,L., Weinstein, A., "Matrixconverters:technologyreview,"IEEE TransactionsOnIndustrial Electronics Vol.49(2)April 2002, pp.276 – 288.
- [13] Wheeler, P.W.,Clare, J.C., Empringham,L,Bland,M.,Kerris,K.G "Matrix converters," IEEE Industry Applications Magazine,Vol. 10 (1), Jan-Feb2004, pp.59 - 65.