

Design and Simulation of a Fuzzy Non Linear PI Controller for Dc-Dc Buck Converter for Low Steady State Deviations and Its Performance Comparison with PI Controller

R. Ganesan¹, S. Vignesh²

Associate Professor, Department of Electrical and Electronics Engineering, Oxford Engineering College, Pirattiyur,
Trichy – 620 009, Tamil Nadu, India¹

Assistant Professor, Department of Electrical and Electronics Engineering, Oxford Engineering College, Pirattiyur,
Trichy – 620 009, Tamil Nadu, India²

Abstract: DC- DC converter plays an important role in power conversion. They are used in many applications like computer power supplies, switching mode regulators etc. Control action of buck converters involves maintaining constant output voltage in the presence of change in load and change in supply voltage conditions. Traditional controllers like PID controllers have limitation in control action whenever the changes are rapid in nature. Fuzzy controllers described by linguistic variables can be employed for buck converter output voltage control. Fuzzy controller can be used as stand alone or in conjunction with PI controller for buck converter output control. In this paper design and simulation using MATLAB/SIMULINK of buck converter output voltage using fuzzy logic control is presented. Simulation results are presented to prove that the steady state deviations of fuzzy PI controller are minimum.

Keywords: Buck converter, PI control, fuzzy control, MATLAB-SIMULINK.

I. INTRODUCTION

The buck converter is a dc-dc converter that is used for stepping down dc voltage level. They are used for mother board supplies in personal computers. They use power electronics switches that can be made on or off to maintain a specified output voltage level. The essence of control action is to maintain desired voltage level in the presence of load side disturbances (load change) and supply side disturbances (input voltage variations). Traditional PID control is employed for buck converter control action. But the control action is subject to component ageing, poor transient response, long settling time and instability in the presence of severe disturbances. Fuzzy controllers are defined by linguistic variables. As such their control action is independent of component ageing. Earlier methods centred around isolation of state variables for feedback control [1]. Uran et al. proposed feedback loop control [2]. Linear Quadratic Controller (LQR) was proposed to improve control action [3]-[4]. Lee C. C. has dwelled upon the application of fuzzy logic control to many control actions [5]. Fuzzy control being a non linear control is independent of measurement and noise error. In addition fuzzy control is adaptable by redefining membership functions. This paper is organized as follows: section 2 describes buck converter circuit and its modeling, section 3 describes fuzzy logic controllers, section 4 describes proposed buck converter control section 5 simulation results and section 6 conclusions.

II. BUCK CONVERTER CIRCUIT

A buck converter is shown in Fig. 1 where the power electronics circuit S is controlled by duty cycle ratio as dictated by output voltage requirement. The buck converter is so called as its output voltage is always lower than input voltage [6].

International Journal of Innovative Research in Science, Engineering and Technology

(An ISO 3297: 2007 Certified Organization)

Vol. 3, Issue 5, May 2014

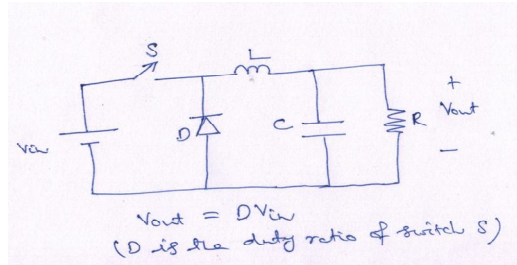


Fig. 1 Buck converter circuit

There are three states of the circuit:

- (i) Switch S on, Diode D off;
- (ii) Switch S off, Diode D on;
- (iii) Switch S off, Diode D off.

Switch S is controlled by pulse width modulation (PWM) technique. In this, a saw tooth voltage is compared with a fixed dc voltage and if saw tooth voltage is lower than the fixed dc voltage then switch S is closed or else switch S is open.

The equations for buck converter are presented as below: [7]

$$i_c = i_L - i_o = C \frac{dv_o}{dt} \quad (1)$$

$$v_c(t) = v_o(t) = \frac{1}{C} \int i_c dt = \frac{1}{C} \int (i_L - i_o) dt \quad (2)$$

$$v_L(t) = \begin{cases} v_i - v_o & \text{if } S \text{ is closed} \\ -v_o & \text{if } S \text{ is open} \end{cases} \quad (3)$$

Entire buck converter is modeled as Matlab Simulink block with V_i, I_o, F as input variables and V_o, I_L as output variables as shown in Fig. 2. F is the gate signal.

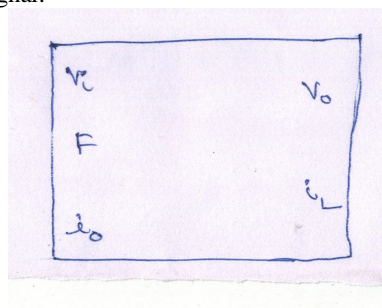


Fig. 2 Buck converter atomic subsystem

International Journal of Innovative Research in Science, Engineering and Technology

(An ISO 3297: 2007 Certified Organization)

Vol. 3, Issue 5, May 2014

III. FUZZY LOGIC CONTROLLER

In 1965, Zadeh proposed fuzzy logic. A simple block diagram of fuzzy controller is shown in Fig. 3.

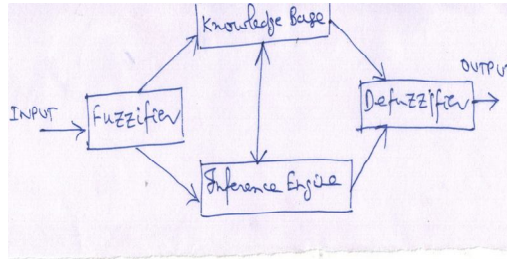


Fig. 3 Fuzzy Control System

Four major units of fuzzy controller are: fuzzification block, fuzzy knowledge base, fuzzy inference engine and defuzzification block [8]. Fuzzification block measures, scales and converts the input into a set of linguistic variables. Fuzzy knowledge base contain fuzzy rule which are in the form of IF----THEN linguistic statements. Fuzzy inference engine is an important block by which fuzzy logic controller mimic human intelligence in decision making. Defuzzification block converts fuzzy control actions into crisp signals.

IV. CONTROLLER FOR BUCK CONVERTER

The buck converter parameters are given in Table 1.

Table 1 Buck converter parameters

Parameter	Value
Input voltage	12 V
Output voltage	5 V
Inductor value	300 μ H
Capacitor value	5 μ F
PWM frequency	50 kHz

The goal of the controller can be stated as follows: To maintain the output voltage constant at 5V in spite of load changes. For the buck converter initially PI controller is used. The design of PI controller is done as follows: Adjust the value of Kp till the desired output is obtained ignoring any offset and adjust Ki to reduce any offset error. A value of Kp=100 and Ki=50 was found to be sufficient. Now PI controller parameters are finetuned with fuzzy logic controller. Error input scaled to -1 to +1 is used as input t fuzzy control block. The input and output membership functions mf1, mf2 and mf3 for Kp and Ki block are presented in Fig. 4. For example the error from -1 to 0 is classified as mf1 with membership value as shown in Fig. 4. The error is presented to fuzzy control block and then multiplied with Kp and Ki value of PI controller in the modified fuzzy PI controller to generate modulating wave of PWM block [9]. The ultimate switching signals are generated by PWM.

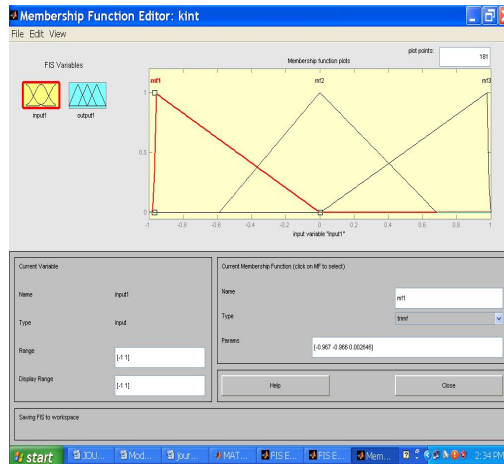


Fig. 4 Fuzzy logic controller membership functions

The rule base is presented as follows:

If input is mf1 then output is mf1

If input is mf2 then output is mf2

If input is mf3 then output is mf3

V. SIMULATION RESULTS

Matlab/Simulink circuit for the buck converter circuit is presented in Fig. 5.

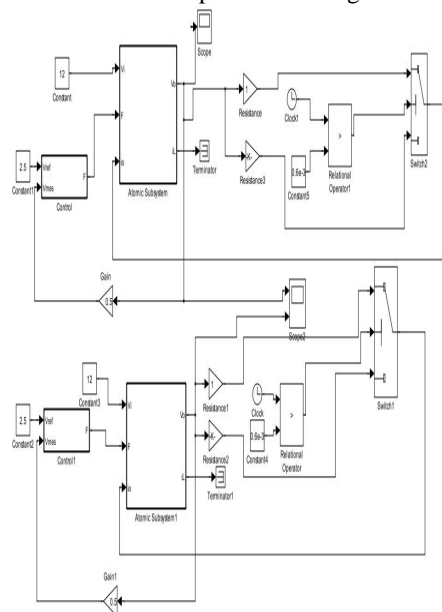


Fig. 5 Buck converter circuit in MATLAB/Simulink

The buck converter block is implemented as separate subsystem. The error is applied to P-I controller. The output of PI controller is given to PWM block. In PWM block the error signal is compared with triangular wave of switching

International Journal of Innovative Research in Science, Engineering and Technology

(An ISO 3297: 2007 Certified Organization)

Vol. 3, Issue 5, May 2014

frequency to generate PWM signals. The fuzzy controller along with PI is shown in Fig. 6. In fuzzy controller block, the error signal is applied first to membership block. The output of the fuzzy block is given to PI controller. PI controller output is given to PWM block to generate switching signals.

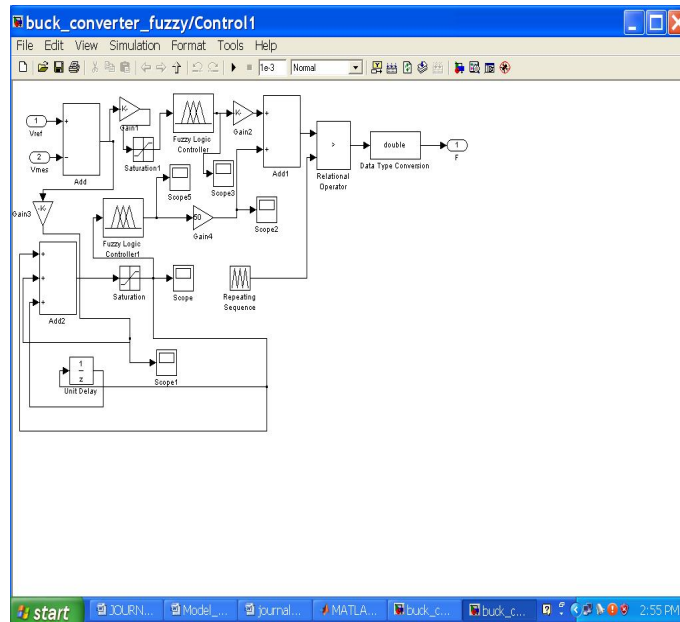


Fig. 6 Fuzzy controller with PI

In the simulation circuit, at 0.6ms the load was changed from $R= 1\text{ ohms}$ to $R=8\text{ ohms}$. Still the buck converter was able to maintain its output voltage. Fig. 7 presents the simulation results. The upper graph shows the simulation of the buck converter with PI controller and the lower graphs shows simulation with fuzzy controller. From the figure, we can infer that fuzzy controller is able to maintain its output voltage at 5V with minimal steady state deviation than PI controller.

From the simulation we can infer that a fuzzy PI controller can maintain tight output voltage keeping steady state error in output voltage to minimum and hence fuzzy PI controller presents a good alternative to be used with conventional PI controllers. Moreover as the control circuit uses linguistic variables, it is not prone to ageing like conventional PI circuits. Additional advantage can be adaptability by simply changing the fuzzy knowledge base.

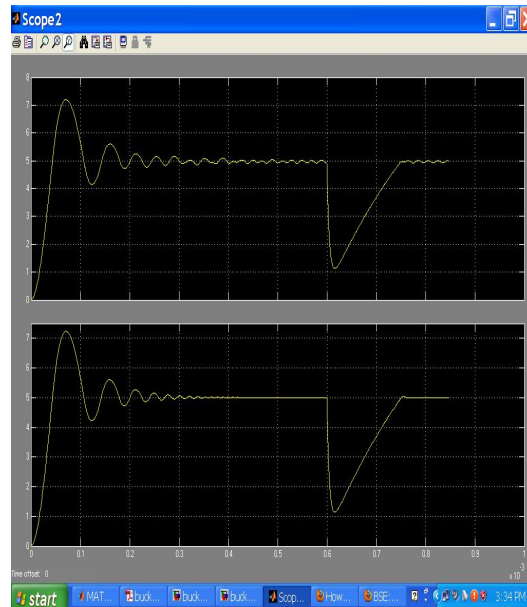


Fig. 7 MATLAB Simulink Simulation of buck converter with PI fuzzy controller

VI Conclusion

In this research paper buck converter controller using PI controller was studied first. Application of fuzzy logic to output voltage error and fuzzy inference of output voltage error was applied to conventional PI block gain K_p and K_i . This greatly improved the steady state error of buck converter output voltage. Finally the simulation results of both converters were compared. From the simulation results it is concluded that application of fuzzy logic to PI controller is an option when tight steady state deviations are needed as in the case of PC power supply.

ACKNOWLEDGMENT

First author wishes to thank his friend Dr. P.S. Srinivasan, Former Dean/EEE, SASTRA University for all his help.

REFERENCES

- [1] Kostov, K. S., Kyyra, J., Suntio, T., The input impedance of a buck converter, European power Electronics Conf., 2003.
- [2] Lasea, I. G. D., Myrzik, J. M. A., State- space control structures for buck converters with /without Input Filter, IEEE. 2005, Power Electron, European Conf., 2005.
- [3] Uran, S., Milanovic, M., State controller for buck converter, EUROCON, 2003., vol. 1, 381–385, 2003.
- [4] Dores Costa, J. m., Design of linear quadratic regulators for quasi- resonant dc-dc converter, IEEE. 2001. Power Electron Conf., PESC 2001. vol. 1, 422 – 426, 2001.
- [5] Leung, F. H. F., Tam, P. K. S., Li, C. K., The control of switching dc-dc converter S-A general LQR problem, IEEE Trans., Ind Electron., vol. 38, 65-71, 1991.
- [6] Lee, C. C., Fuzzy Logic in Control Systems: Fuzzy Logic Controller- Part I, IEEE Trans. On Syst. Man Cybern., vol. 20, no. 2, pp. 404-418, March/April 1990.
- [7] M. Bayati Poodeh, S. Eshtehardiha, M. R. Zare Application of Fuzzy Logic to Control the DC-DC Converter, 7th WSEAS International Conference on Electric Power Systems, High Voltages, Electric Machines, Venice, Italy, November 21-23, 2007
- [8] Shubhangi V. Patil Tejashree R. Shulkla Paresh J. Shah, Modeling Of PID Controller Based SMPS Using FPGA, International Journal of Innovative Research in Science, Engineering and Technology Vol. 1, Issue 2, pp. 159-168, December 2012

International Journal of Innovative Research in Science, Engineering and Technology

(An ISO 3297: 2007 Certified Organization)

Vol. 3, Issue 5, May 2014

- [8] Cetin Elmas a, Omer Deperlioglu , Hasan Huseyin Sayan, Adaptive fuzzy logic controller for DC–DC converters Expert Systems with Applications, 36 (2009), pp. 1540–1548
- [9] So, W. C., Tse, C. K., & Lee, Y. S. A fuzzy logic controller for DC–DC converter. In IEEE power electronics specialists conference records 1994, pp. 315–320.
- [10] Ned Mohan, T.M. Undeland and W.P. Robbins, Power electronics, converters application, and design, John Wiley and sons, third edition, 2010.
- [11] M.H. Rashid, ,Power electronics circuits, devices and application third edition, Pearson education, 2012.
- [12] Terano, T., AsaI, K., Sugeno, M., Applied fuzzy systems, Academic Press Inc., pp.86-93, 1994.

Biography



R. Ganesan was born in India in 1970. He received his B.E. and M.Tech.degree from Alagappa Chettiar College of Engineering and Technology, Karaikudi and IIT, Kanpur respectively. He worked as Assistant Project Engineer (Electrical) at The Fertilizer Corporation of India Limited, A Government of India Undertaking. Presently he is working as Professor and HOD/EEE at Oxford Engineering College, Pirattiyur, Trichy – 620 009.



S. Vignesh was born in India in 1989. He received his B.E.degree from Saranathan College of Engineering. He is presently working as Assistant Professor at Oxford Engineering College, Pirattiyur, Trichy – 620 009.